



MEGALON: Efficient Data Sharing for Partly Coherent CXL Memory

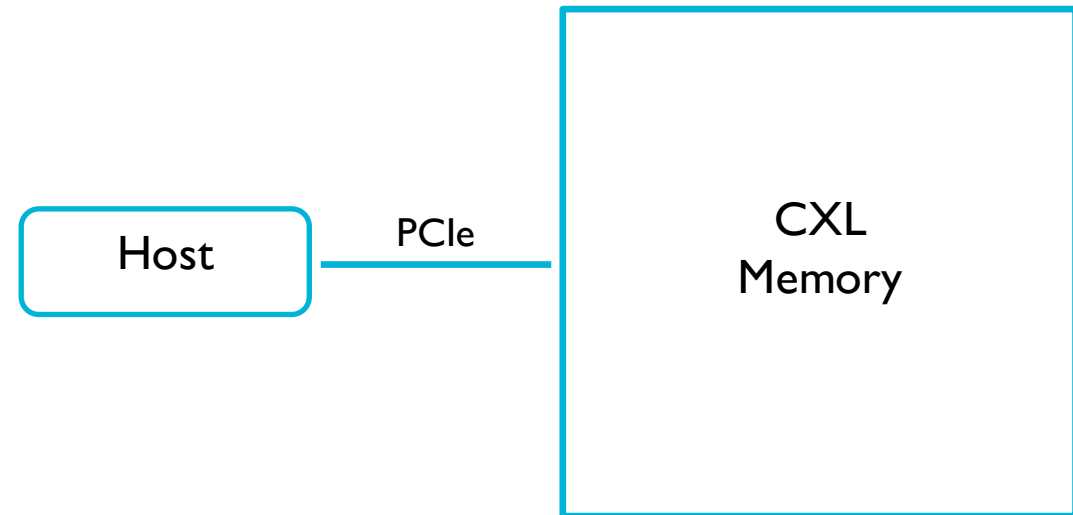
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Marcos K. Aguilera¹, Ram Alagappan, Aishwarya Ganesan

University of Illinois Urbana-Champaign

¹NVIDIA

Compute Express Link (CXL) Background

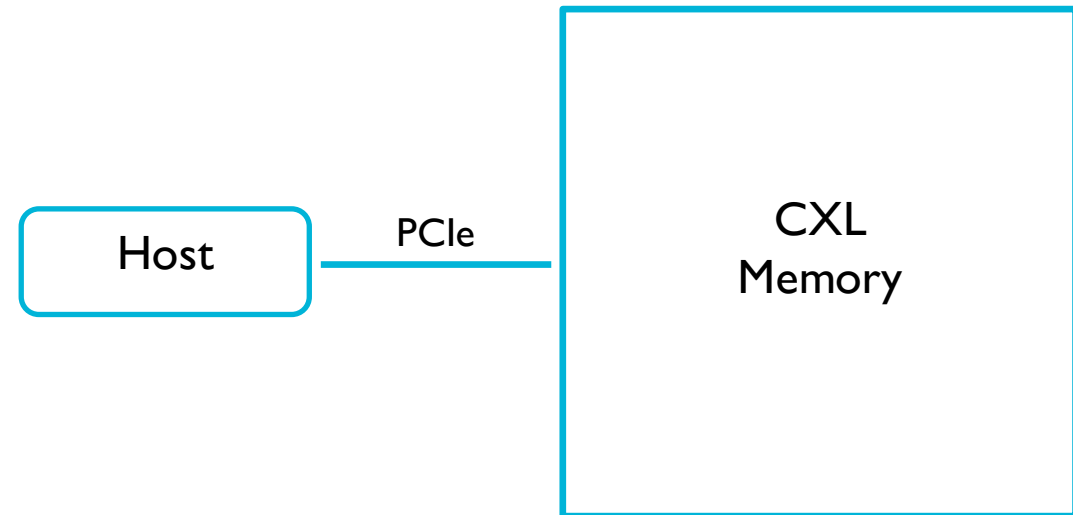
CXL memory: a memory device over CXL attached to compute hosts



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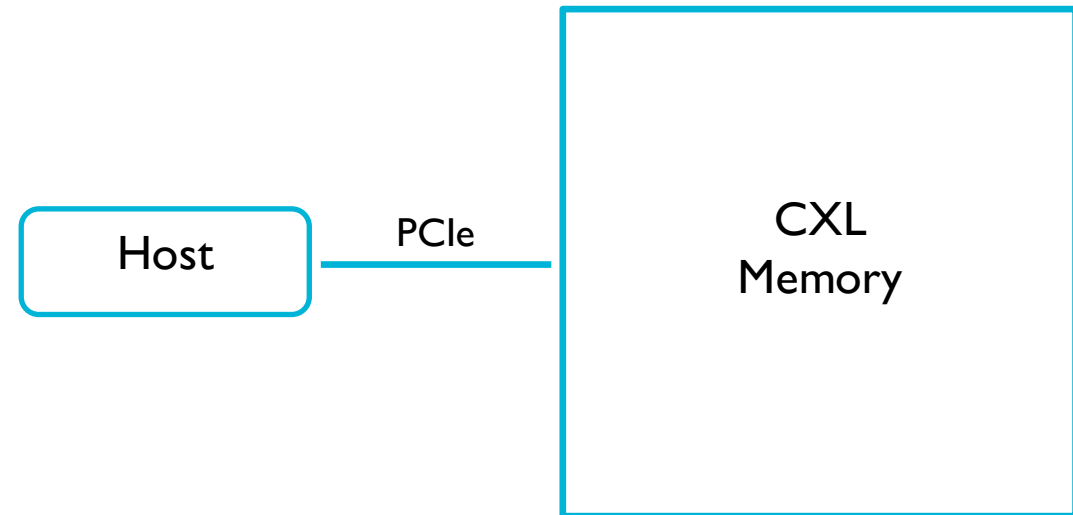
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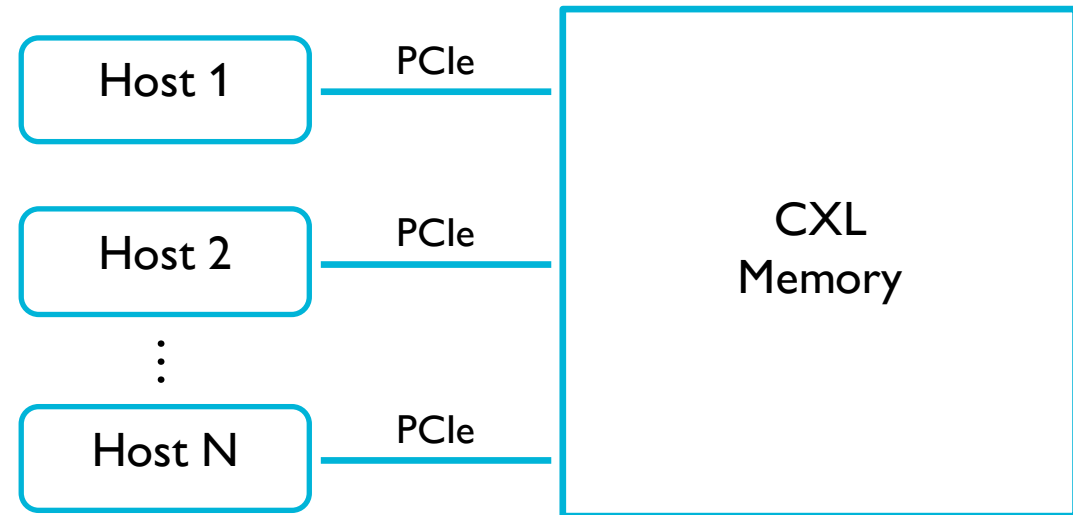
CXL memory: a memory device over CXL attached to compute hosts

- CPU loads/stores — direct access, zero data copies
- Function as memory expander



CXL 3.0: Memory Sharing Across Multiple Hosts

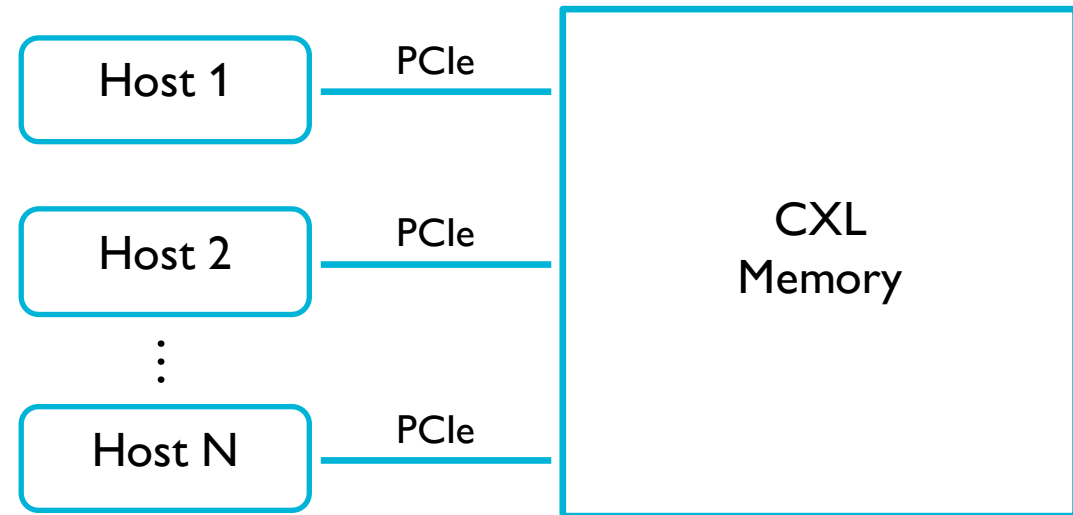
CXL 3.0: TBs of **shared** memory across **multiple** hosts



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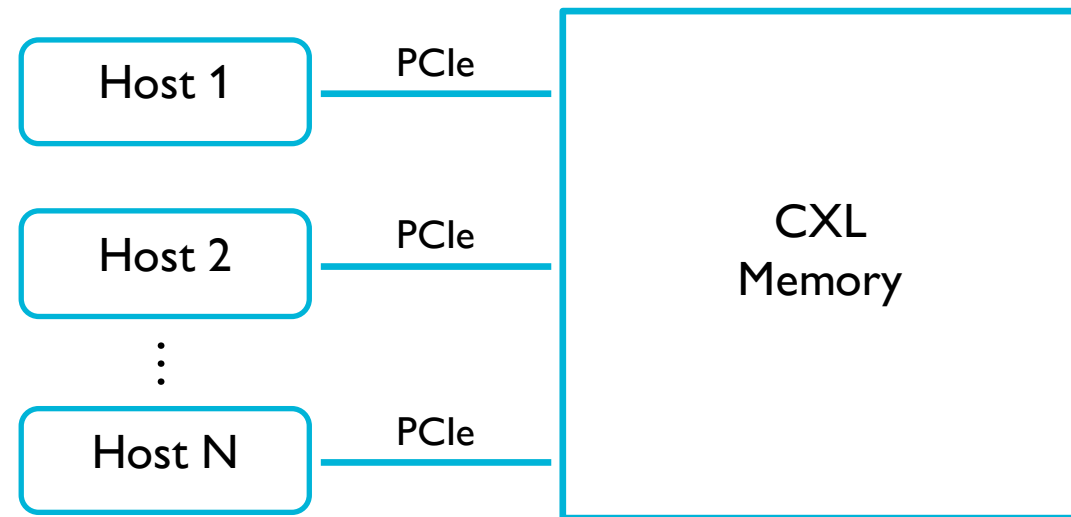
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CXL 3.0: Memory Sharing Across Multiple Hosts

CXL 3.0: TBs of **shared** memory across **multiple** hosts

- CPU loads/stores — direct access, zero data copies
- Efficient multi-host databases, key-value & object stores



The Partly Coherent CXL Model

Hardware coherence covers only a small slice of CXL 3.0 memory



CXL

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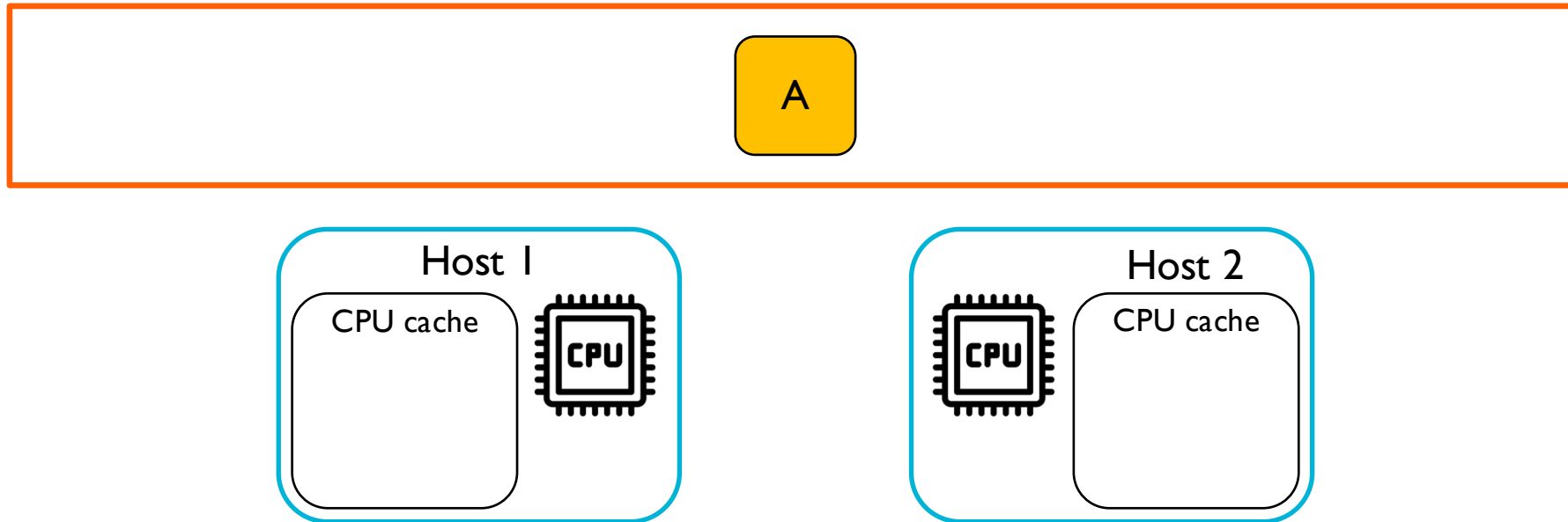
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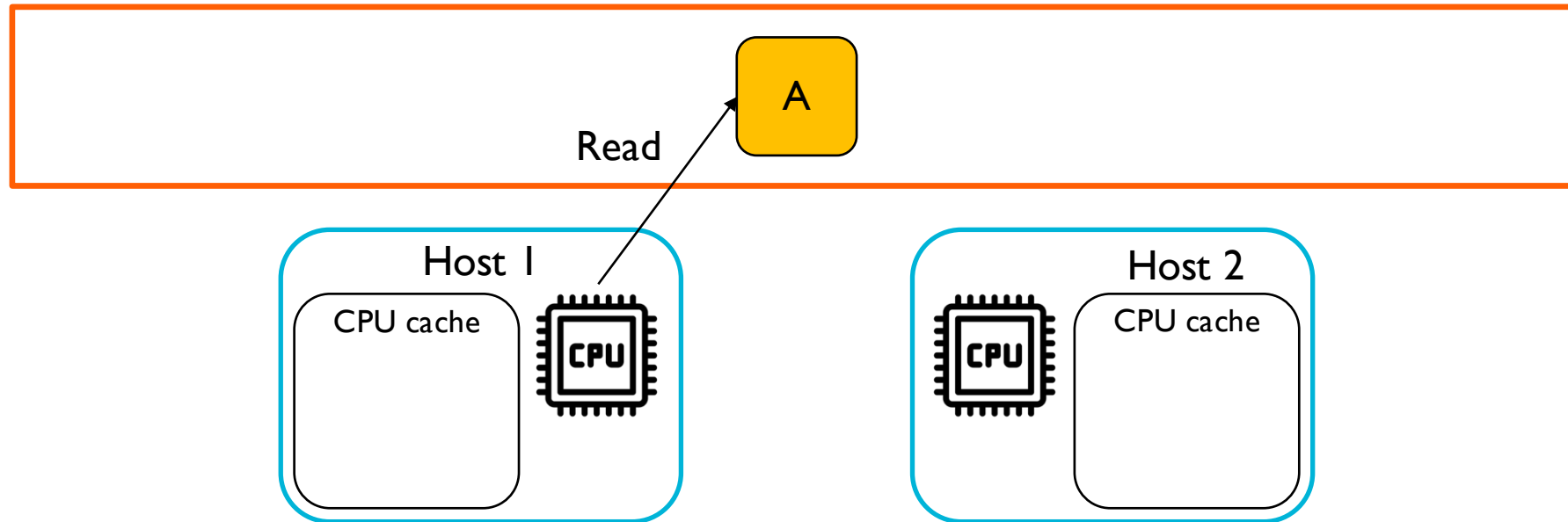


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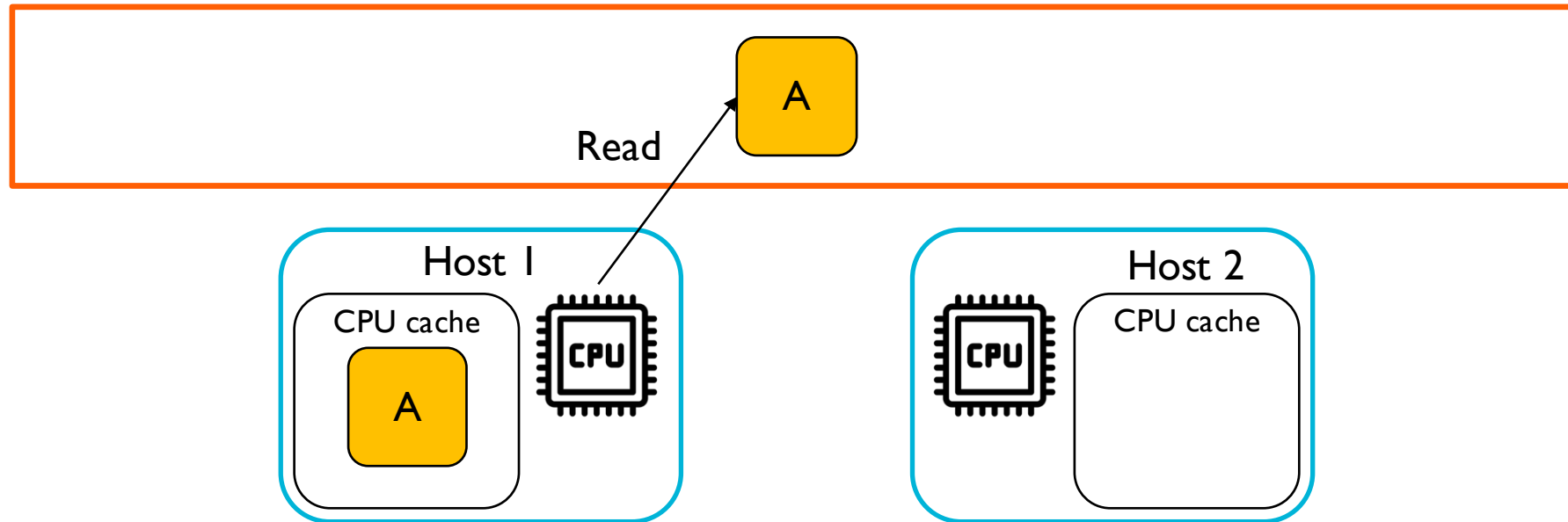


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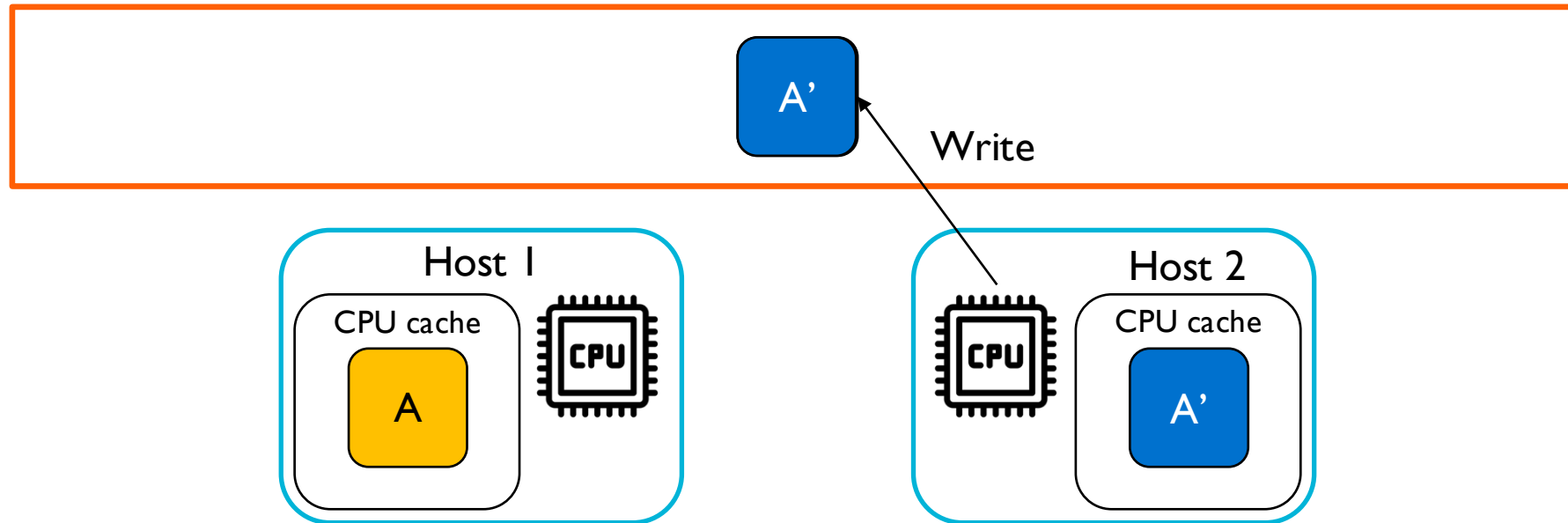


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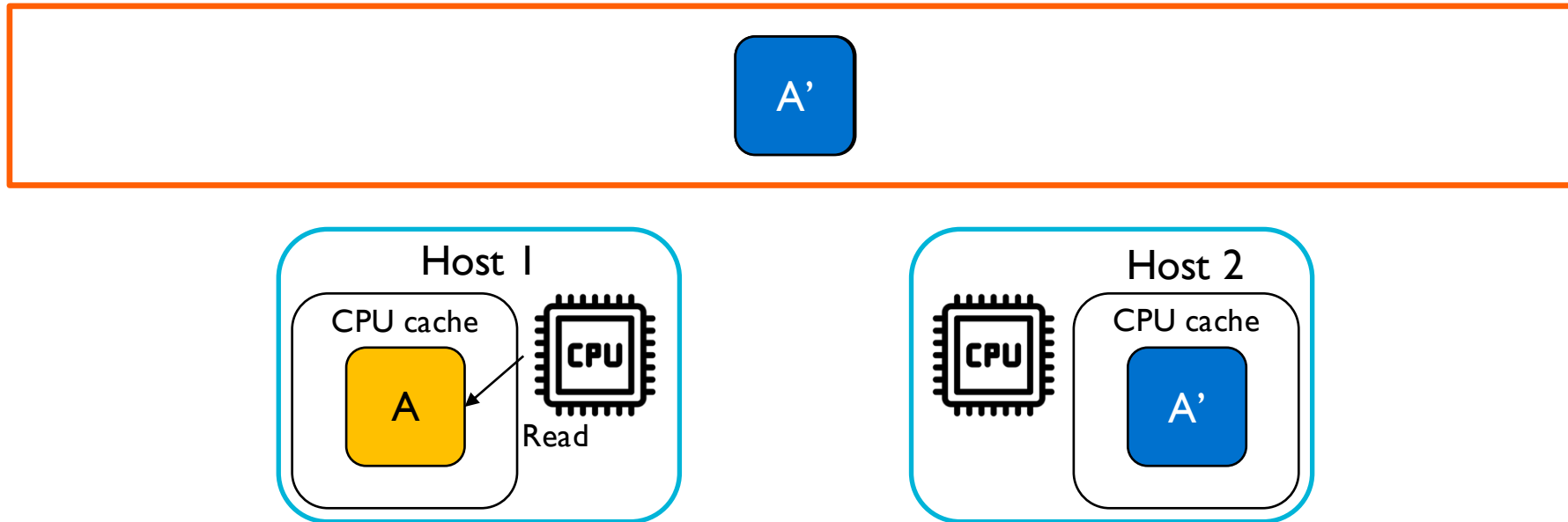


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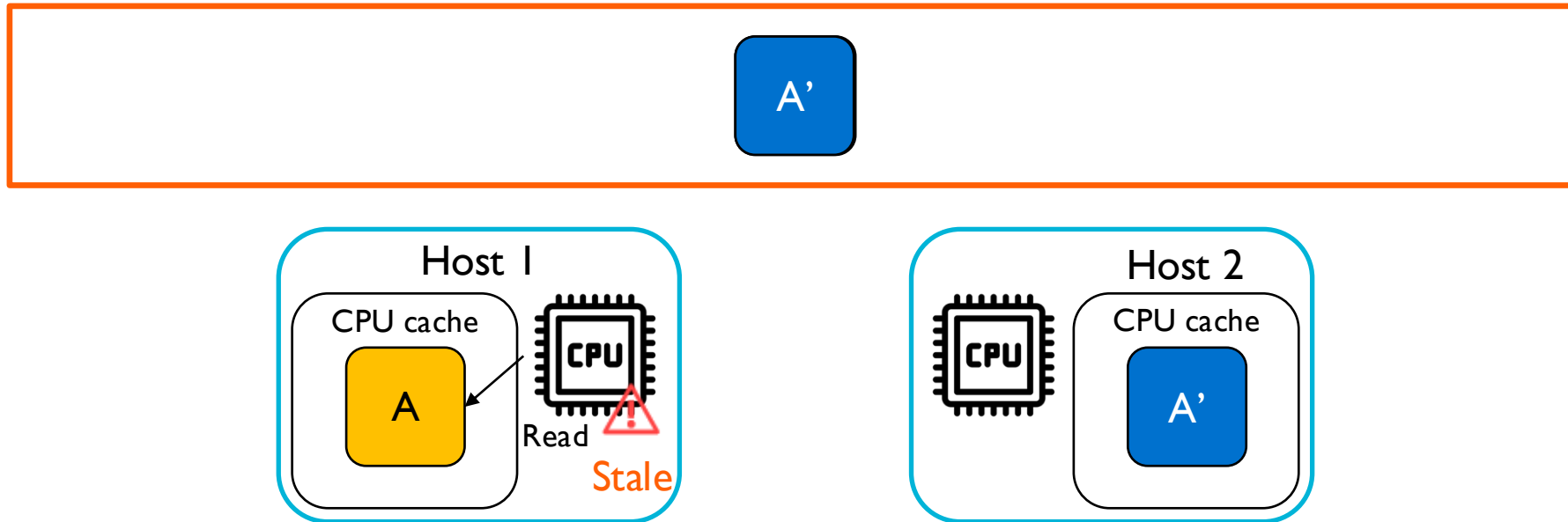


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How to Coherently Share Data?



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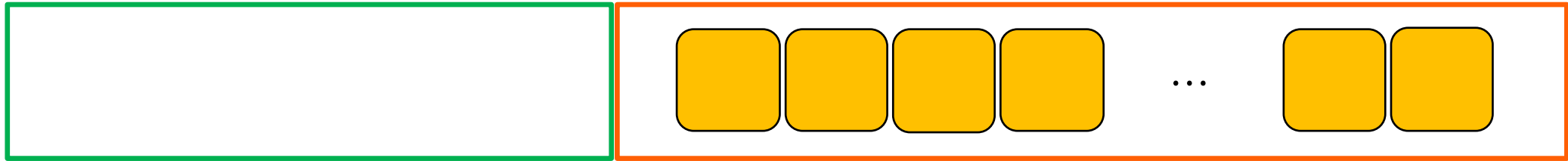
Restrict data sharing to SCR?

Too restrictive! Cannot utilize LNR capacity

A Natural Approach: Software Coherence

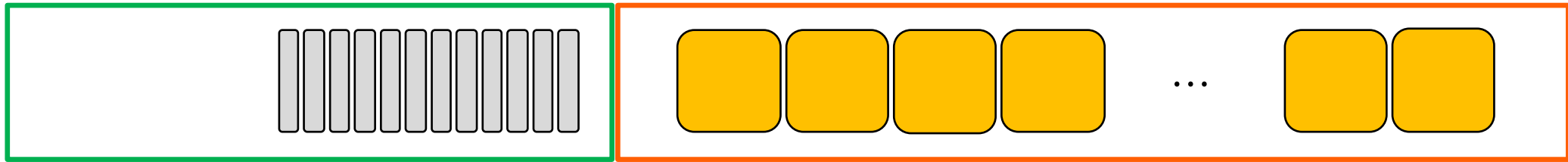


A Natural Approach: Software Coherence



Data objects in LNR
(coarse-grained: e.g. DB rows,
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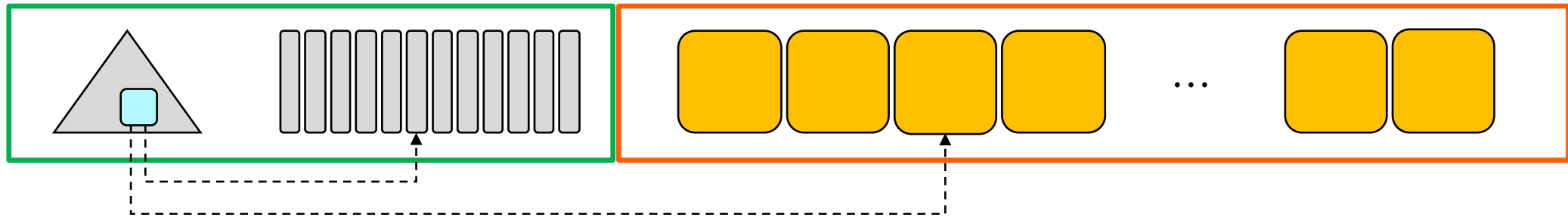
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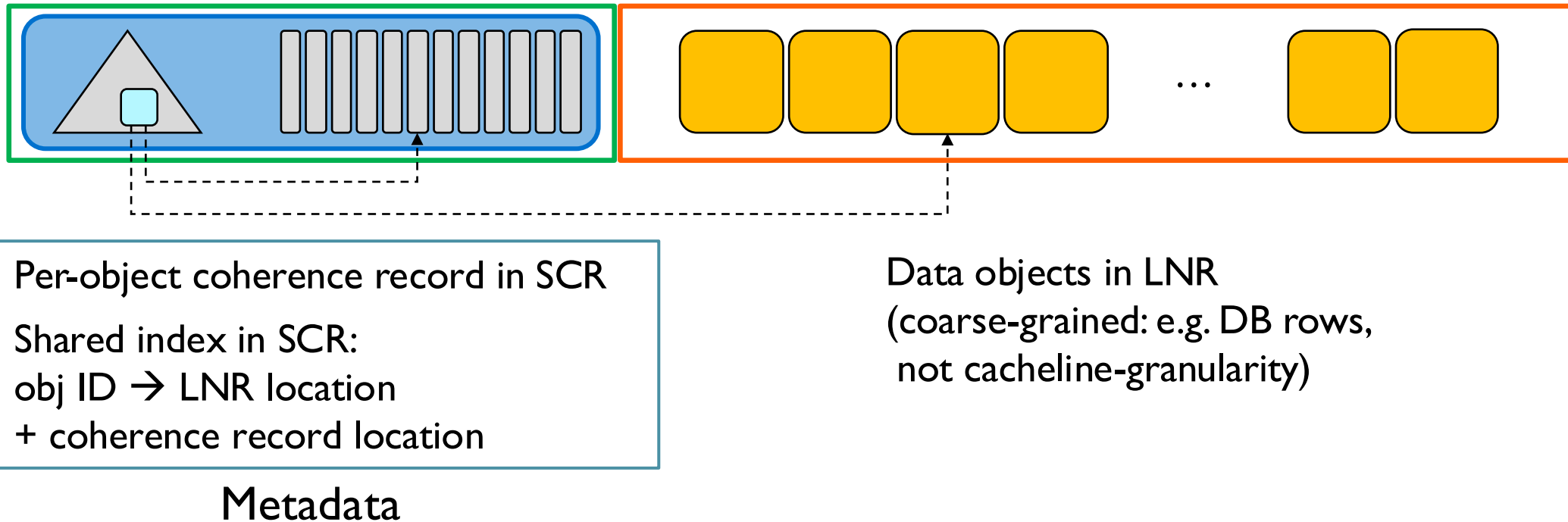
obj ID $\rightarrow$ LNR location

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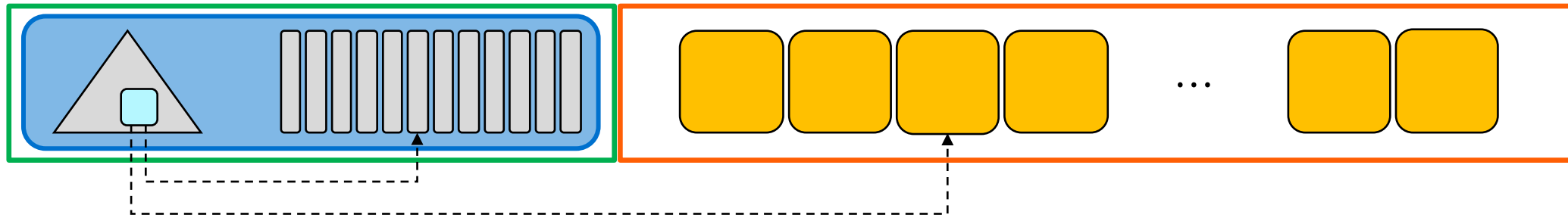
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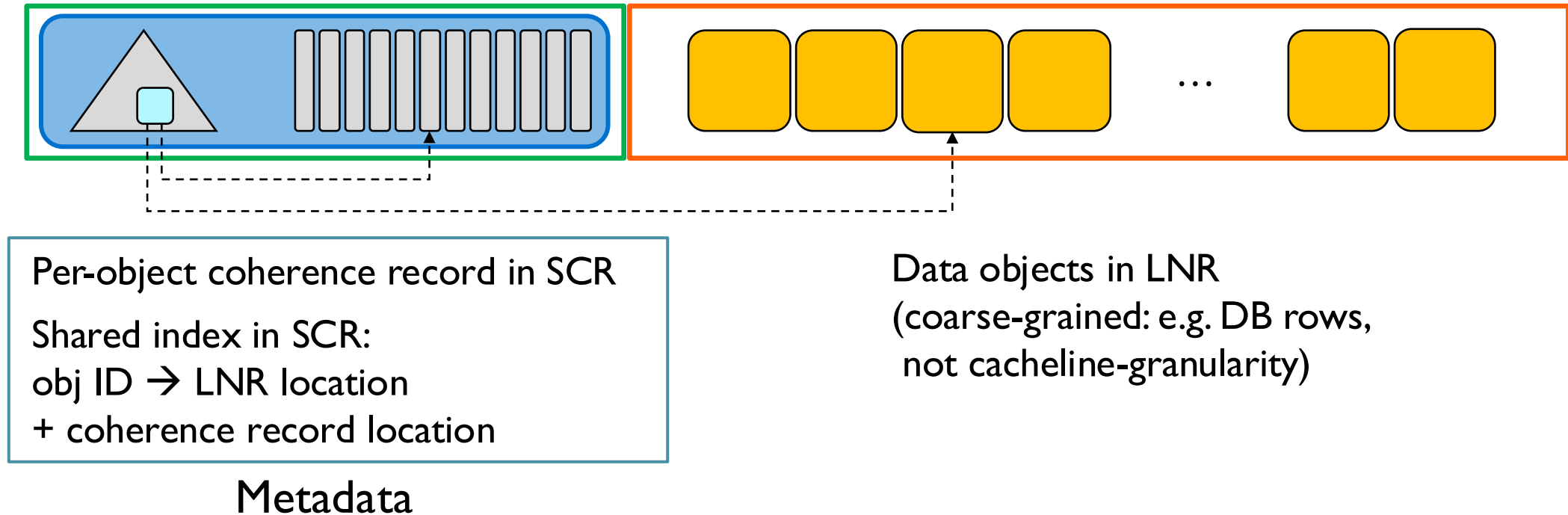
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Metadata

HCMeta: Hardware-Coherent Metadata-Based Sharing

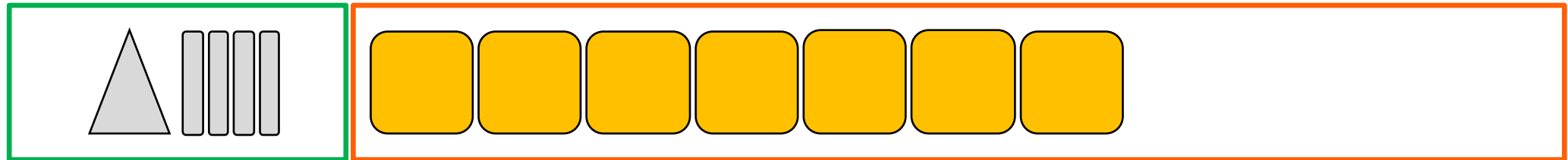
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HCMeta: Hardware-Coherent Metadata-Based Sharing

Tigon^[1], the first database for partly coherent CXL, uses a similar approach

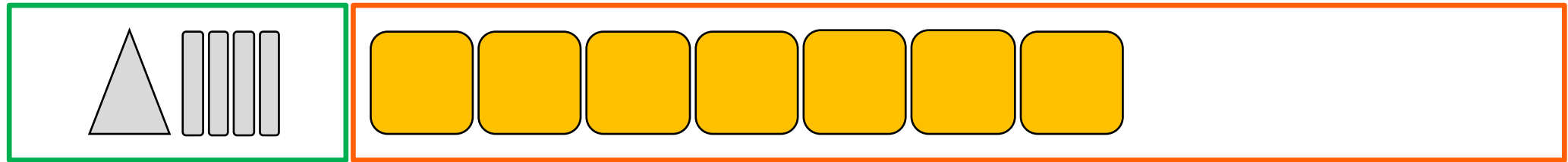
Drawbacks of HCMeta



A few hundred MBs

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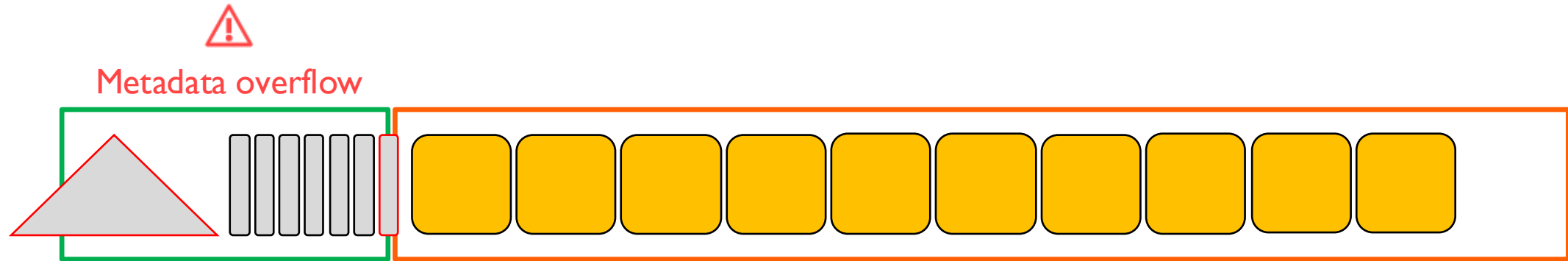
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SCR capacity is tiny (a few hundred MBs)

- With a large dataset, metadata is 100x larger than SCR capacity

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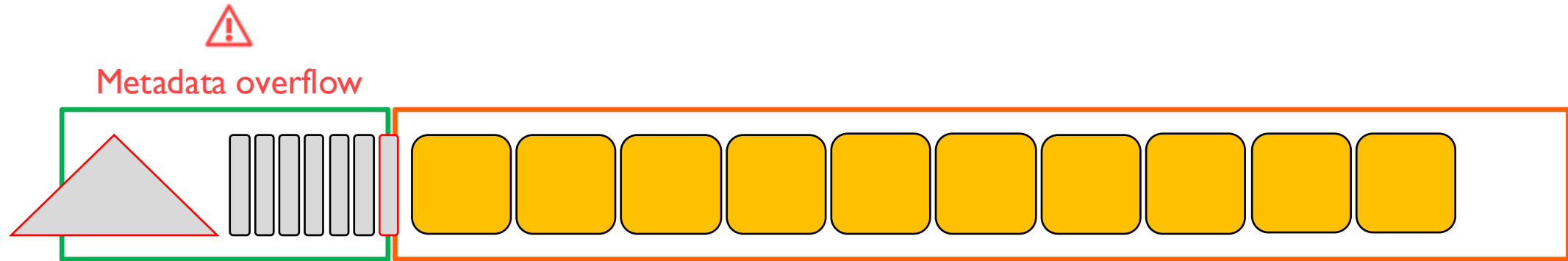


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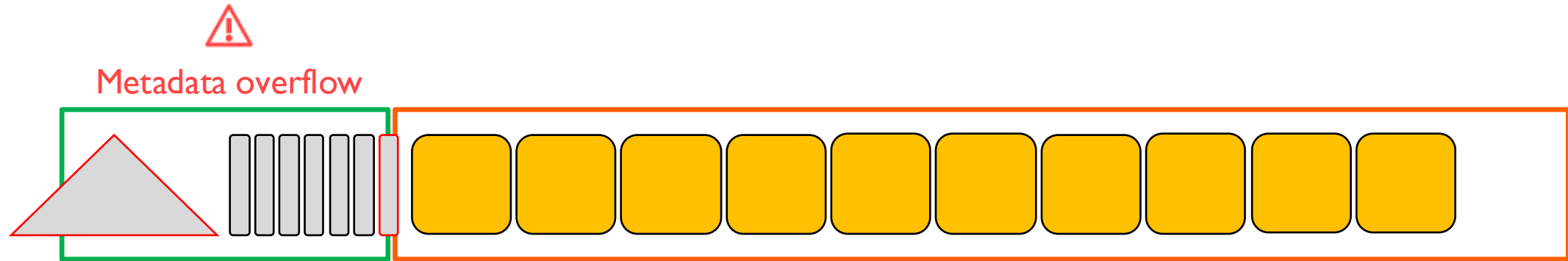
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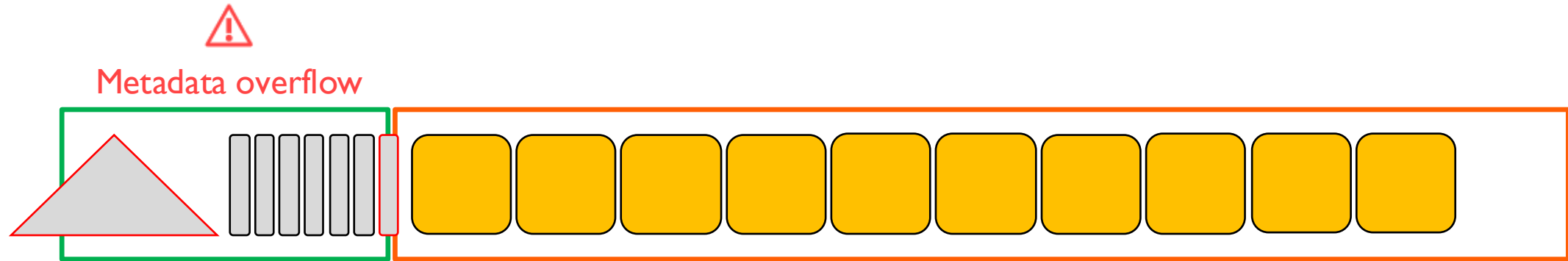
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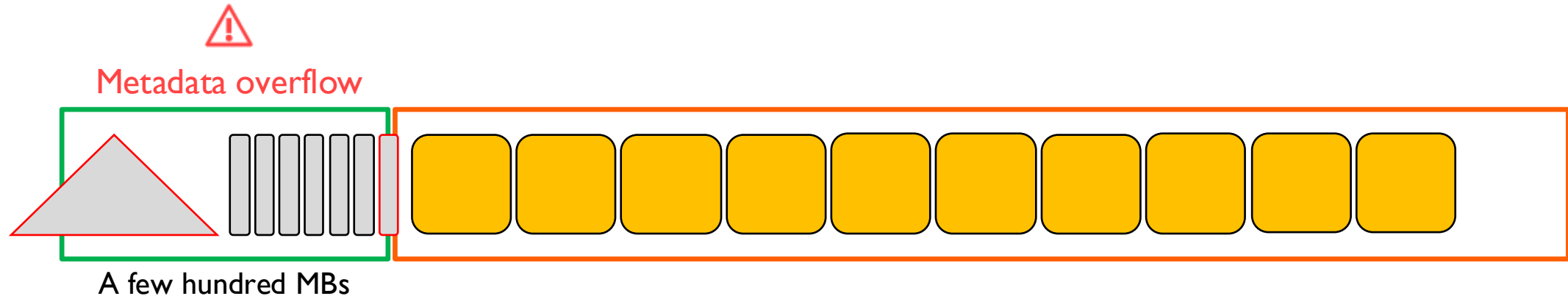
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Not an issue for Tigon because data sharing is occasional for cross-partition transactions

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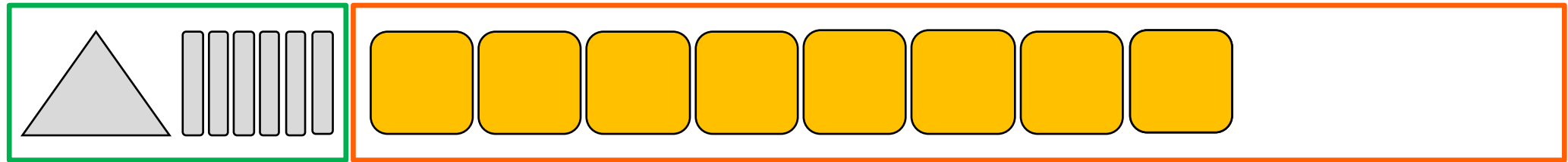
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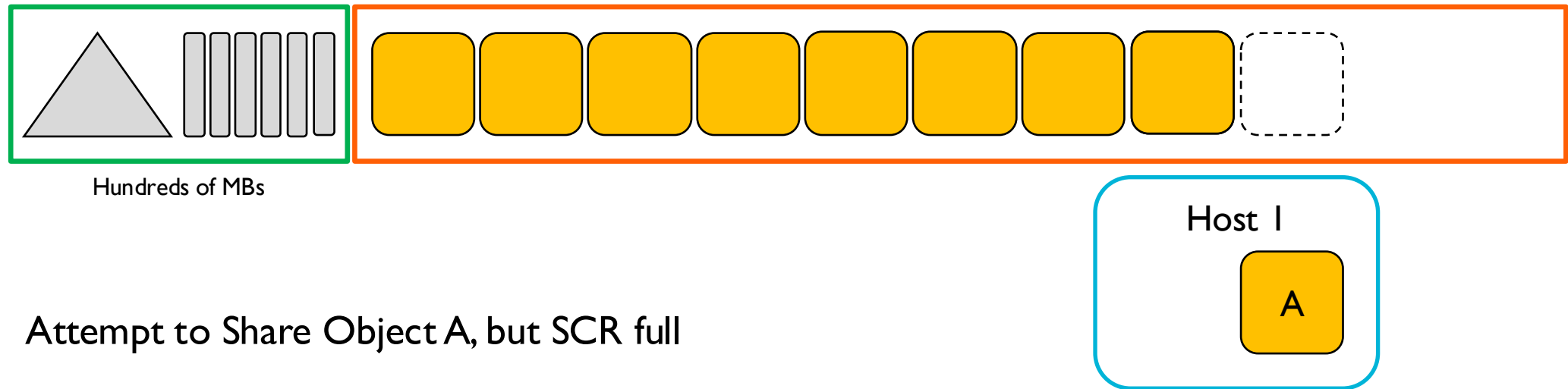
But limiting for general sharing of large amount of data

How HCMeta Handles Metadata Overflow?

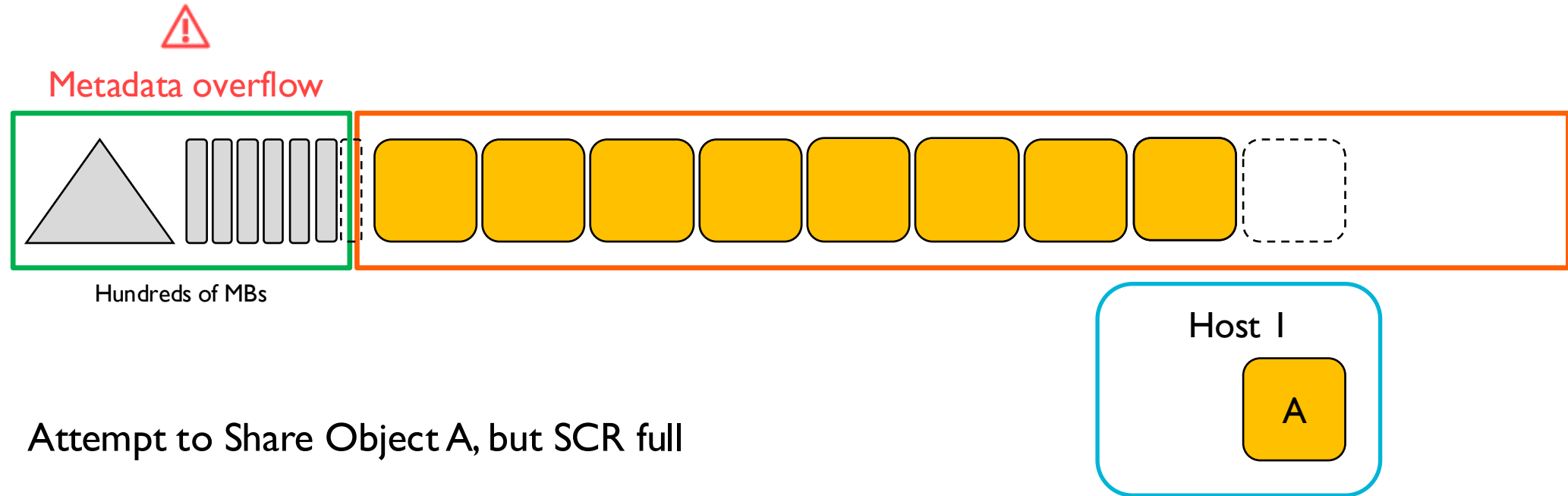


Hundreds of MBs

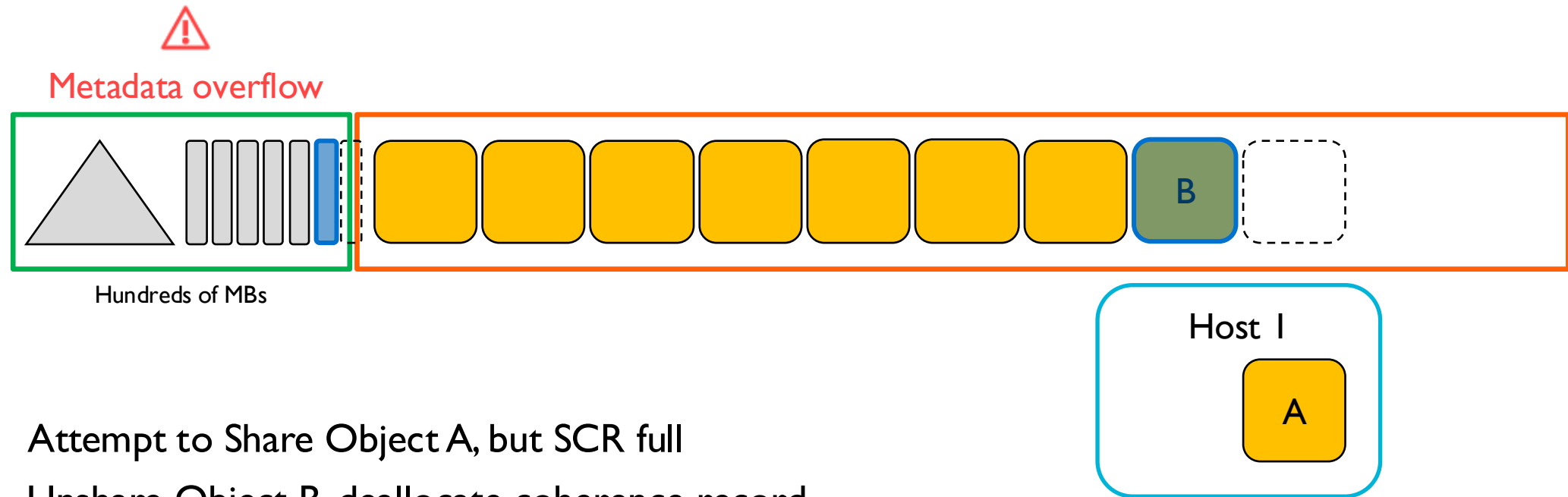
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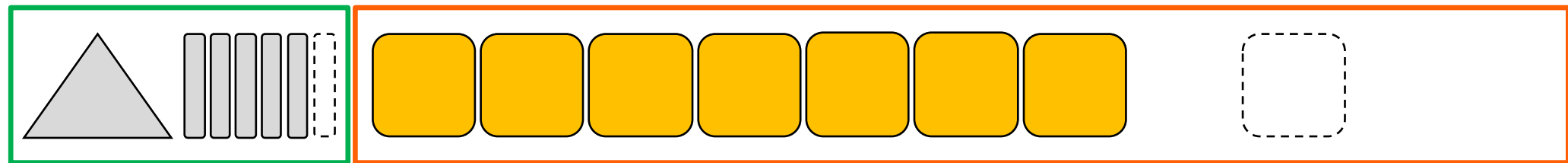
How HCMeta Handles Metadata Overflow?



Attempt to Share Object A, but SCR full

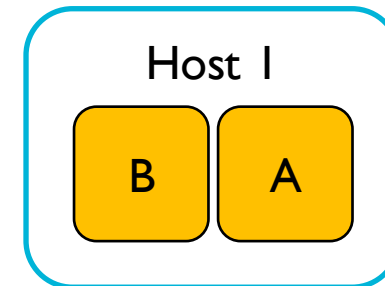
Unshare Object B, deallocate coherence record

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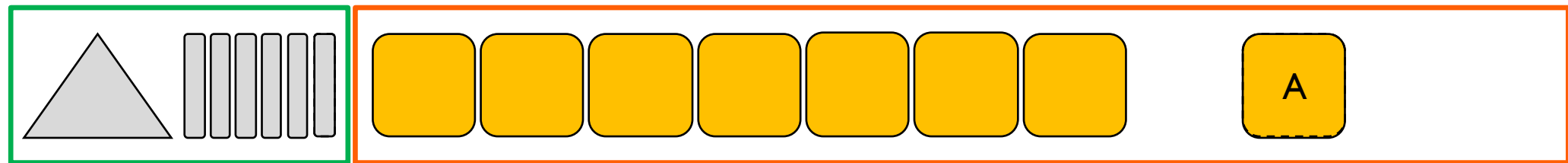


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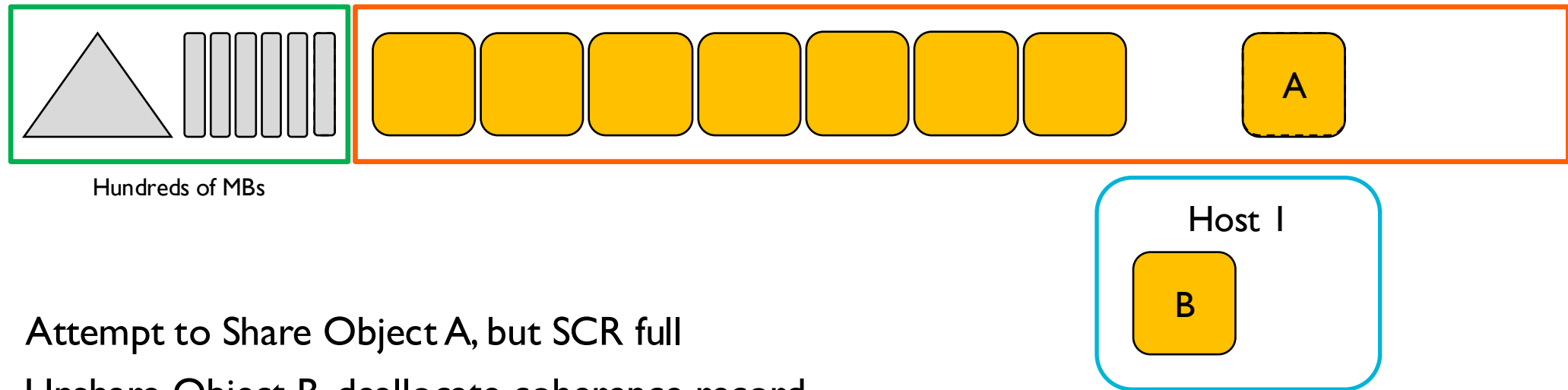
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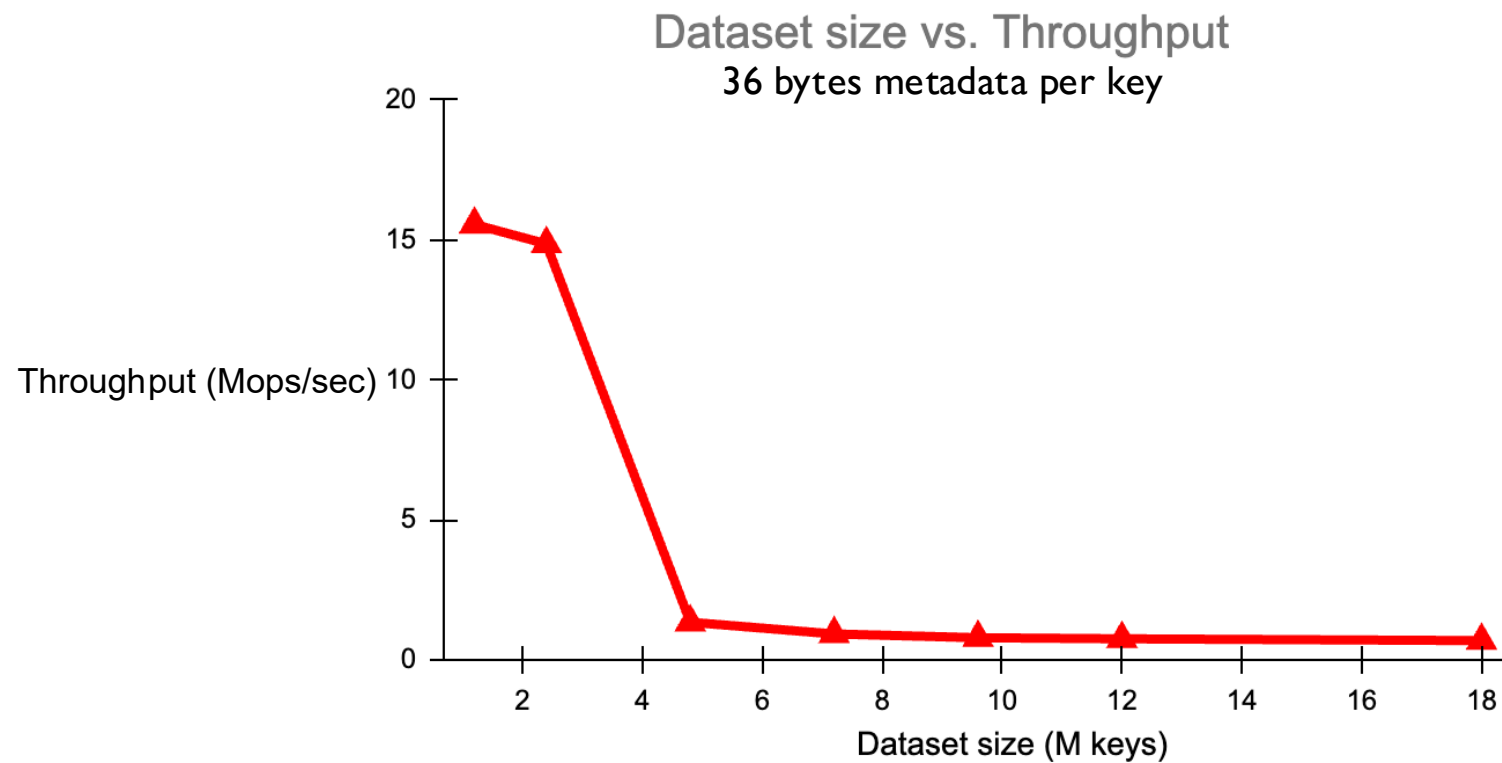
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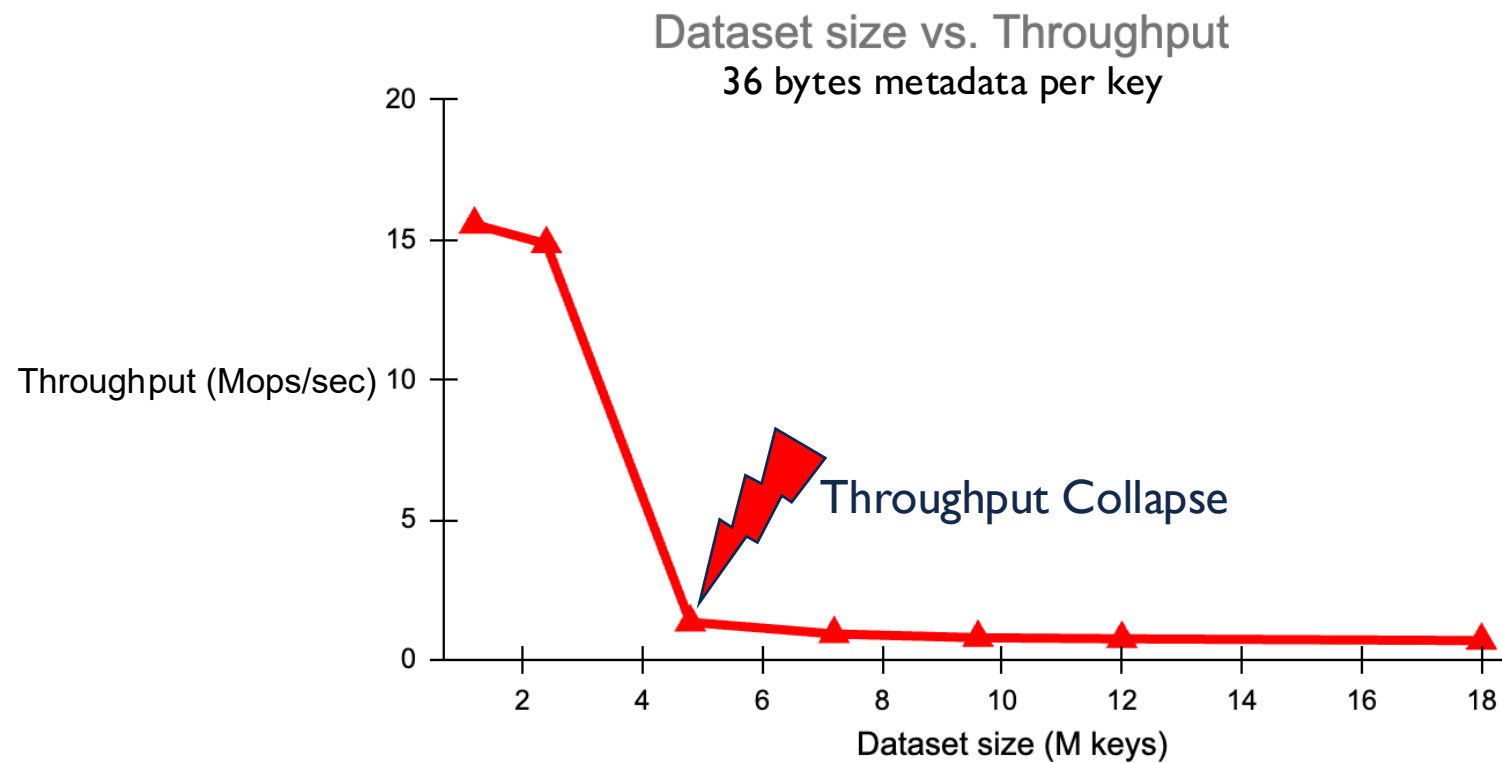
Such repeated sharing and unsharing (churns) will happen with large datasets

Impact on Performance



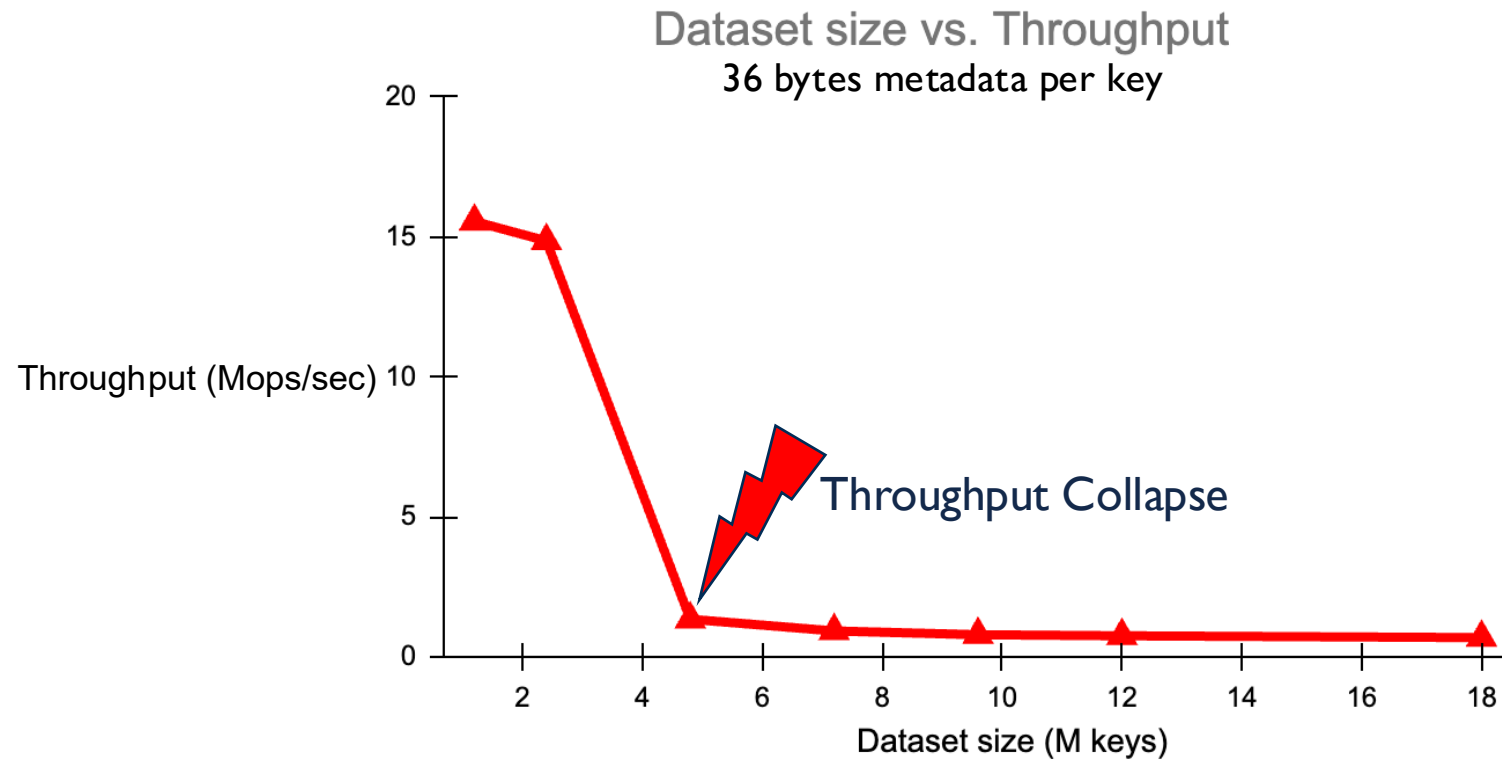
HCMeta: 100 MB SCR capacity

Impact on Performance



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HCMeta is not performant when a large number of objects need to be shared

How can one efficiently share a large number of objects in CXL
when even the metadata is too big to fit in SCR?

How can one efficiently share **a large number of objects** in CXL
when even **the metadata is too big to fit in SCR?**

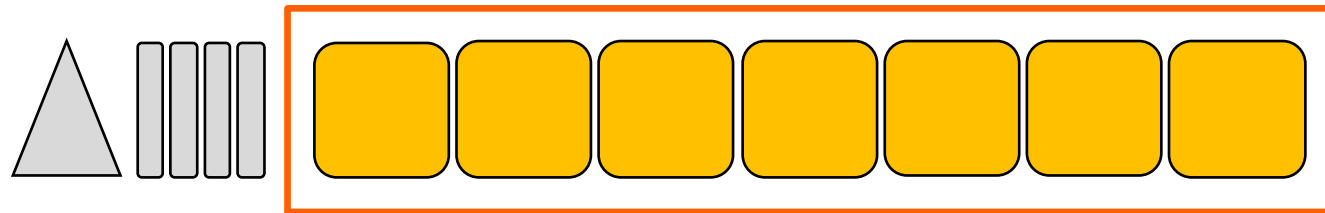
We answer this with:

MEGALON

A new data-sharing approach for partly coherent CXL

MEGALON Overview

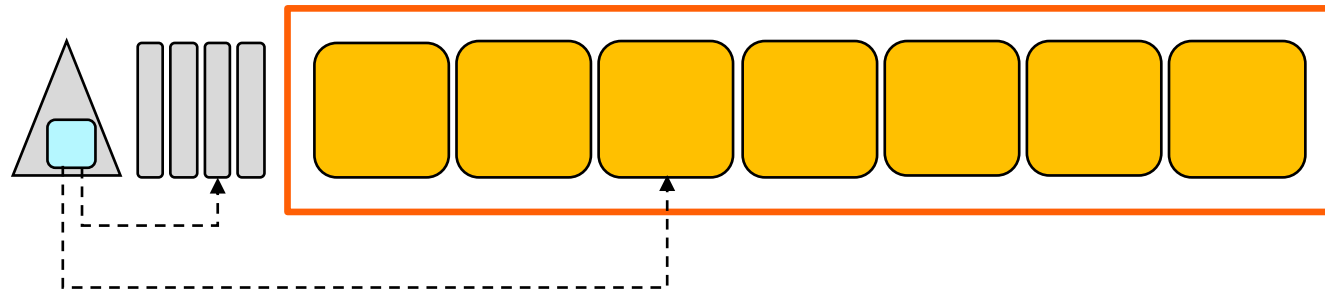
Like HCMeta, share data objects via shared metadata



MEGALON Overview

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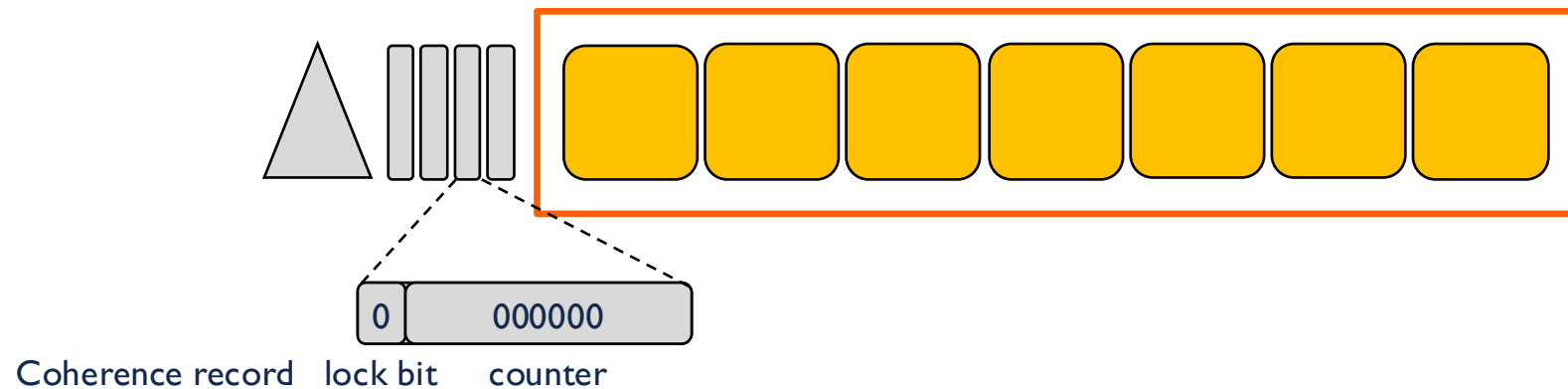
Index: object id $\rightarrow$ coherence record, object location in LNR



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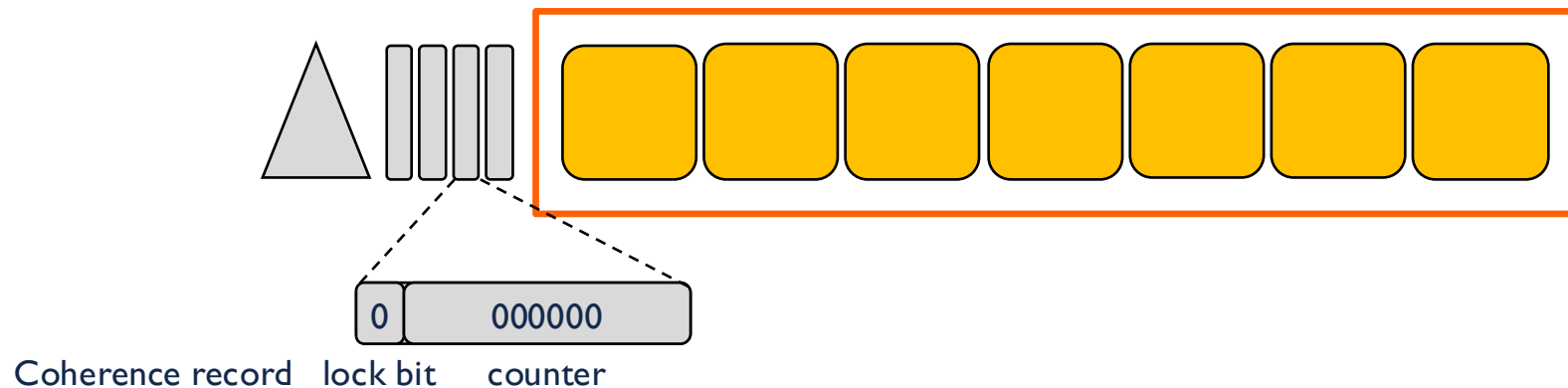
MEGALON Overview

Like HCMeta, share data objects via shared metadata

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How software coherence works:

- Lock bit to serialize writes
- Writers increment the counter before and after the write
- Readers use the counter to detect read-write conflicts and stale processor caches (and then flush)





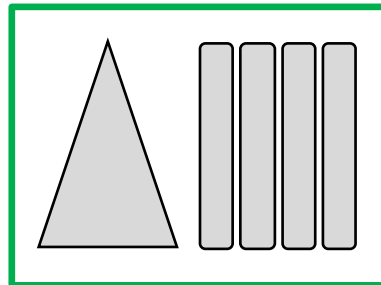
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Novelty: How MEGALON shares the metadata

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Observation: Two kinds of metadata



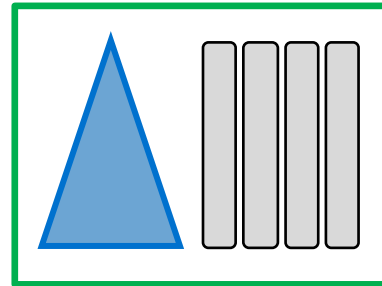
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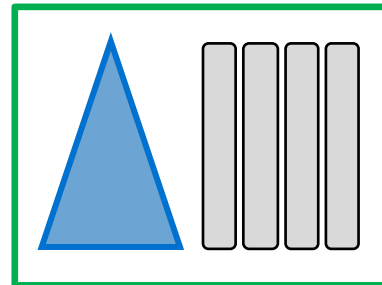
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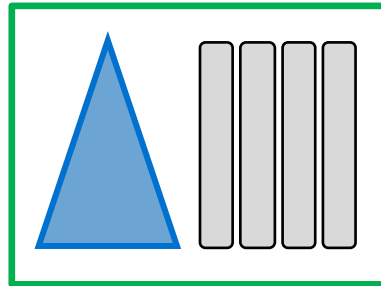
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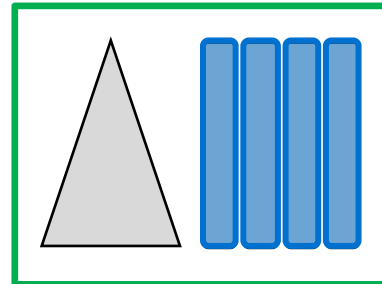
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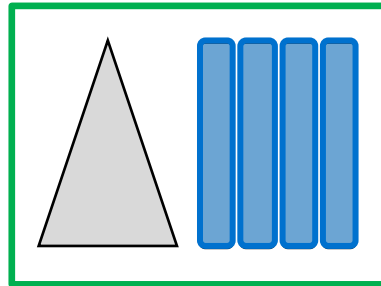
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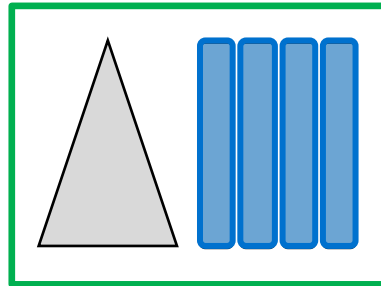
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Coherence Record

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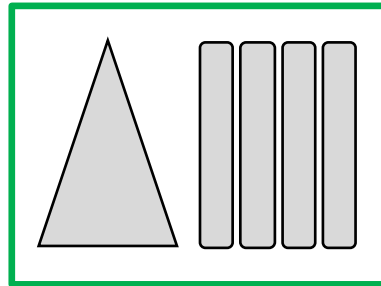
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HCMeta: Indistinguishably keeps all metadata in SCR

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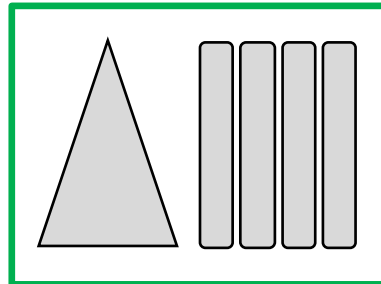
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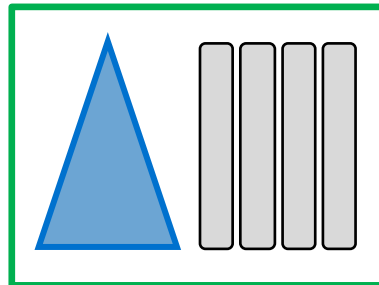
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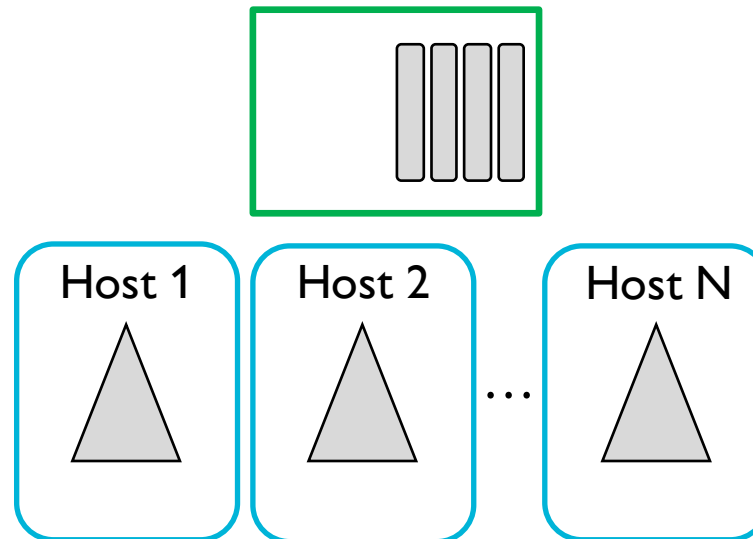
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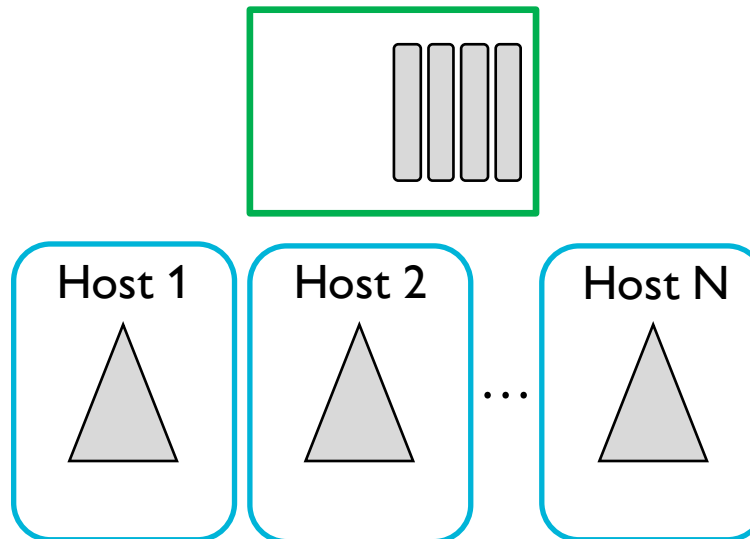
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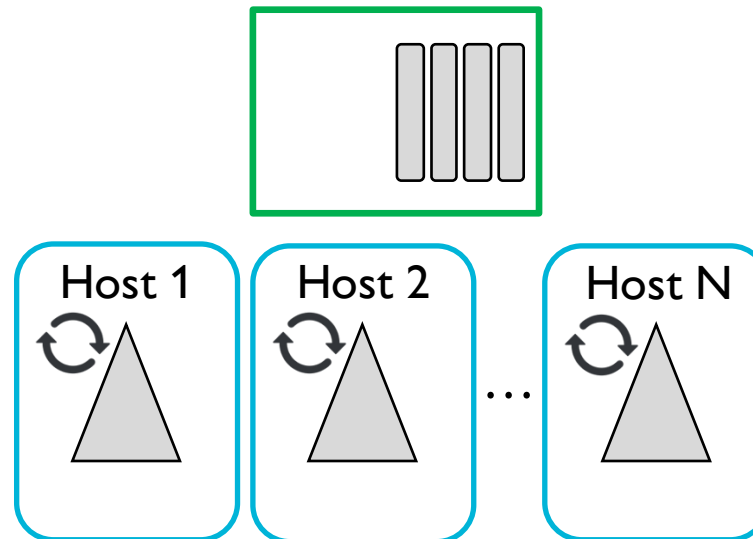
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Observation: Two kinds of metadata

Key Idea: Split Metadata Sharing

Index

Large but updated infrequently

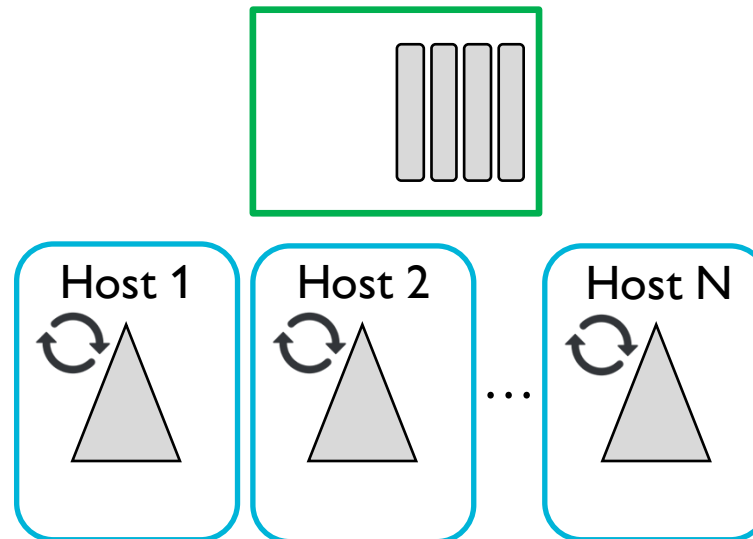
- Contains object ID
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Logically share - Replicate

Hosts have ample DRAM

Cheap synchronization

HCMeta: Indistinguishably keeps all metadata in SCR



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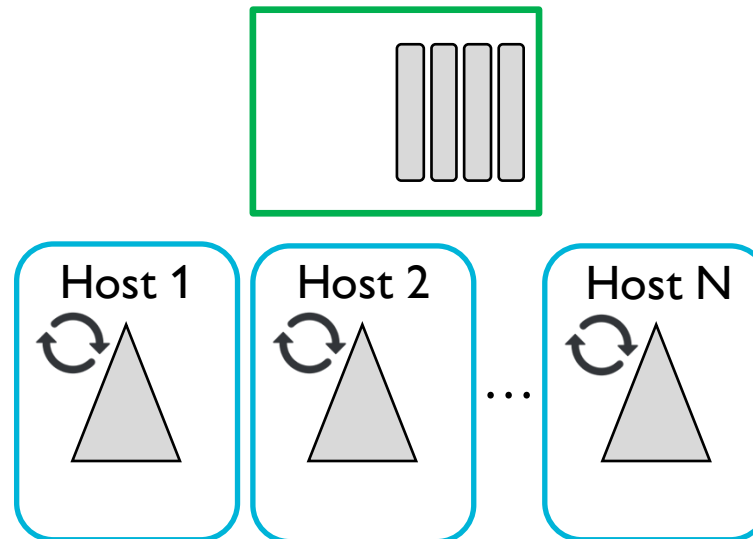
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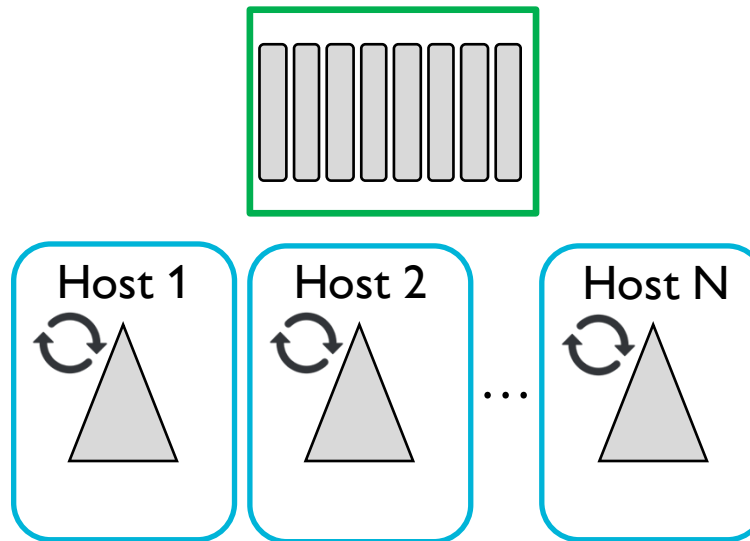
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Result: Share significantly more objects than HCMeta

Challenges

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CI. How to keep the index replicas consistent?

Challenges

C1. How to keep the index replicas consistent?

C2. What to do when SCR for coherence records runs out?

Challenges

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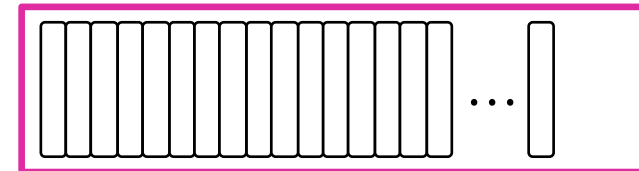
C2. What to do when SCR for coherence records runs out?

MEGALON's solution:

A Shared Log in CXL

What is a Shared Log?

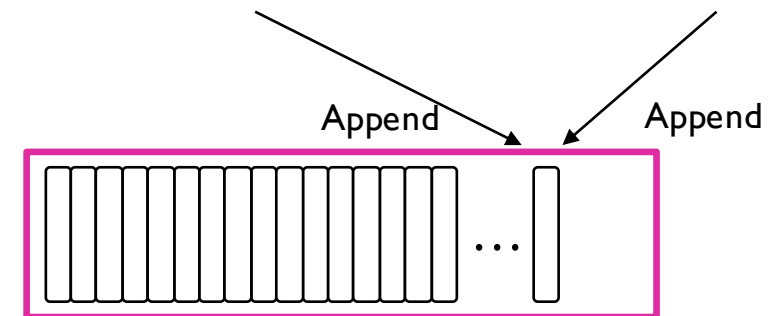
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What is a Shared Log?

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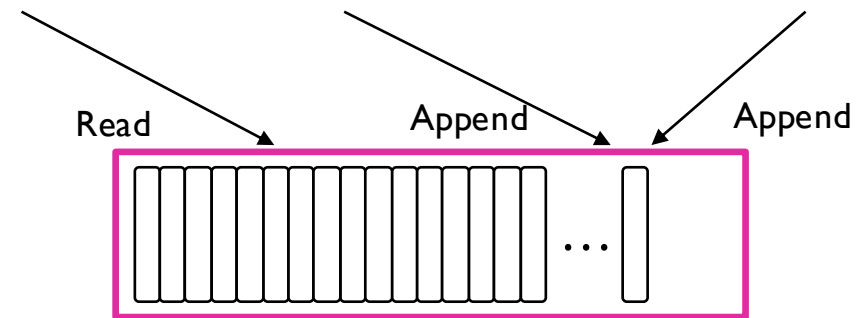
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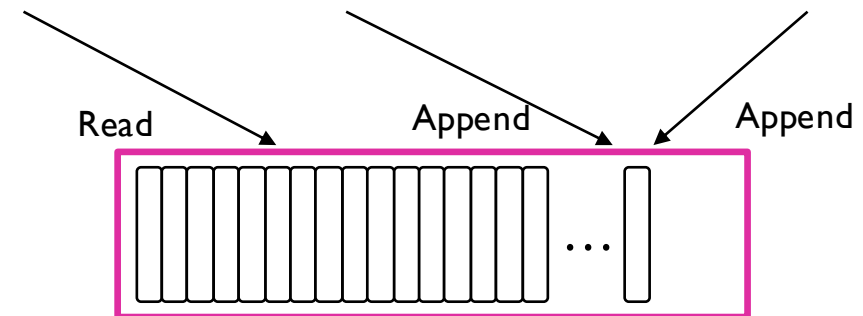


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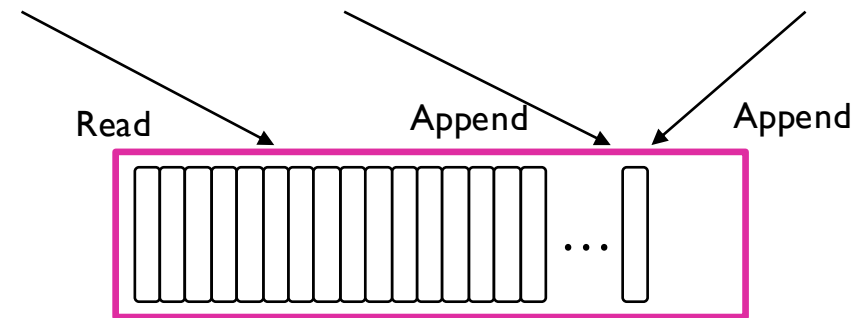
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State machine replication can be built on top of shared log

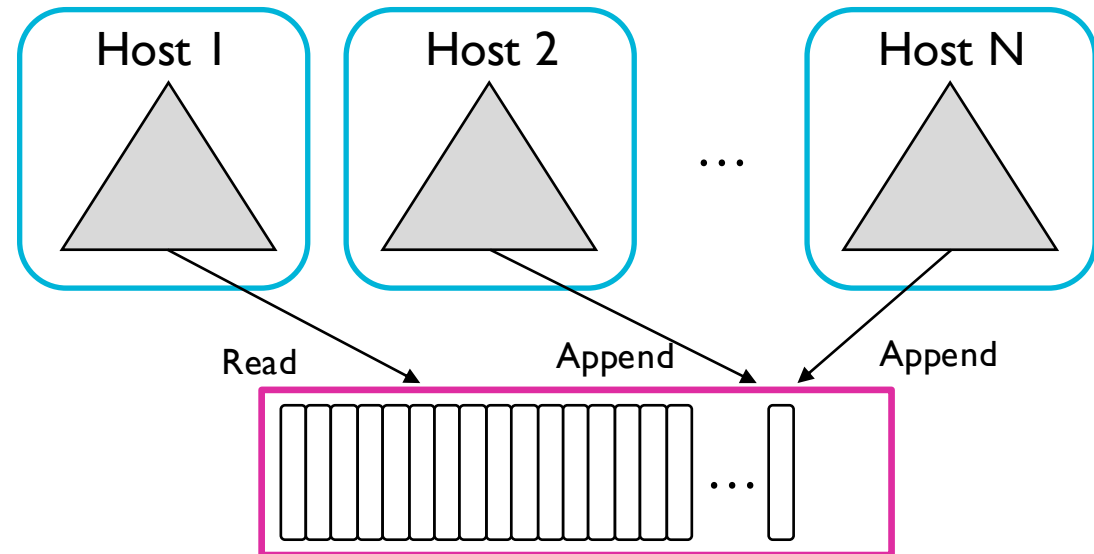


CI. Index Consistency on top of Shared Log



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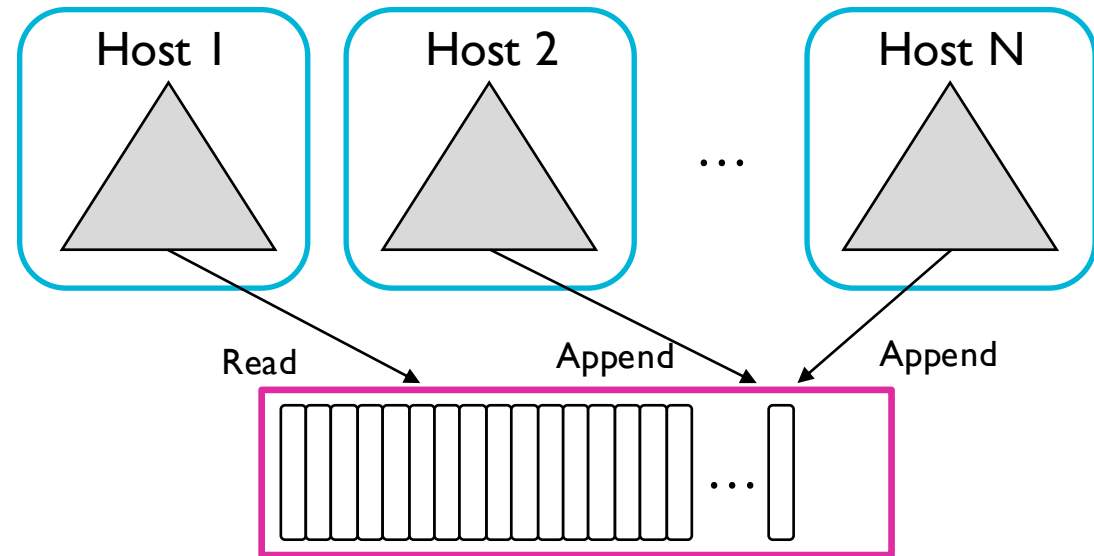
Use shared log to synchronize logical index replicas through state machine replication



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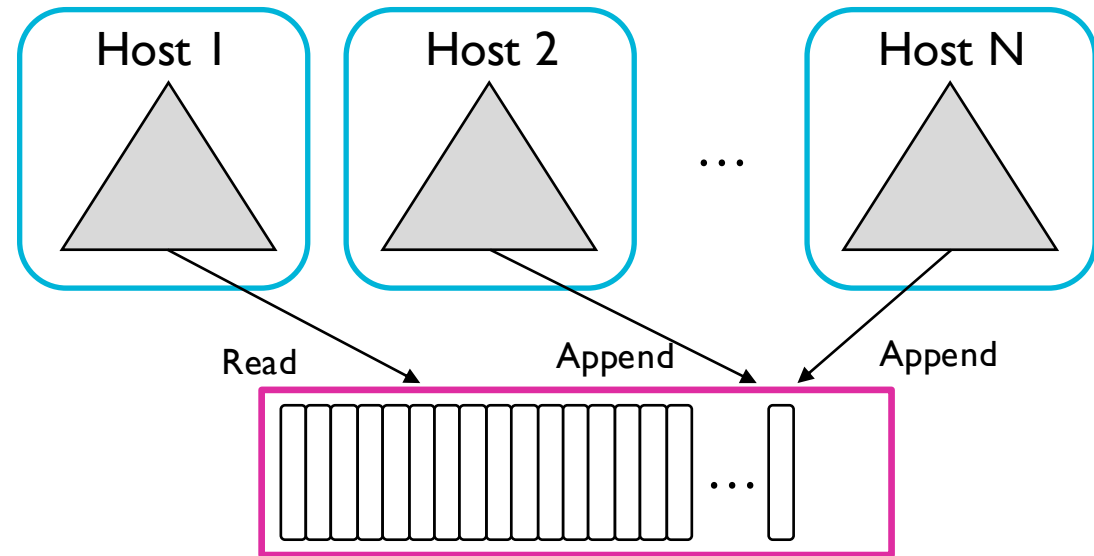
- All index operations ordered through shared log



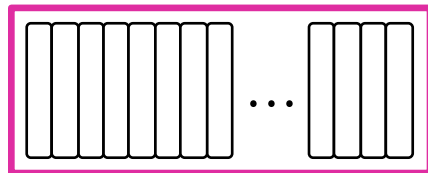
CI. Index Consistency on top of Shared Log

Use shared log to synchronize logical index replicas through state machine replication

- All index operations ordered through shared log
- Synchronize by replaying log



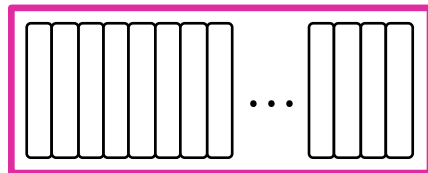
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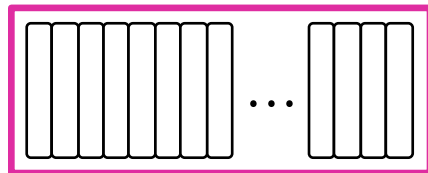
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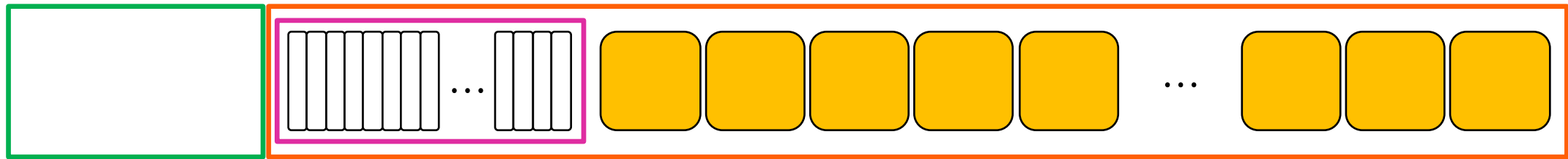
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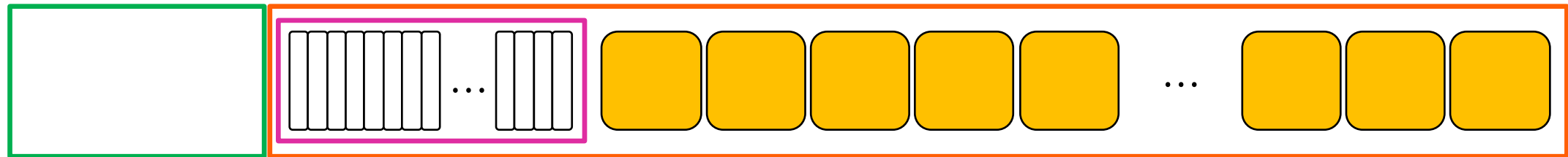
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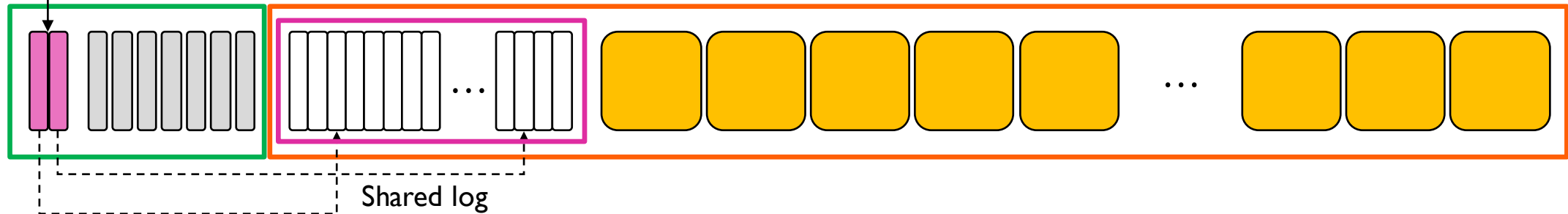
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Shared log
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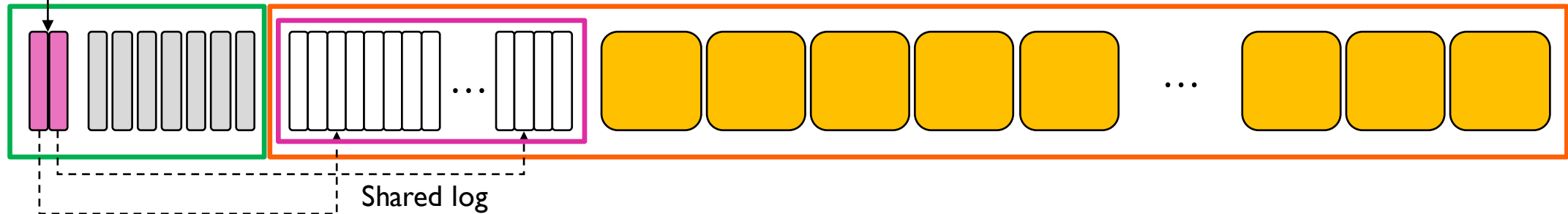
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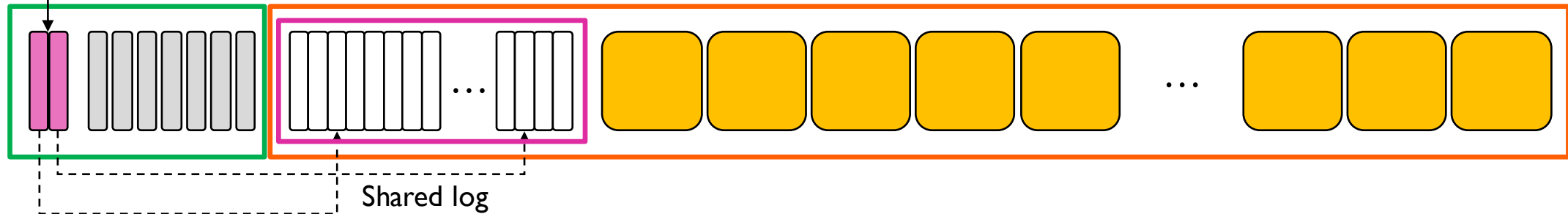
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Also small SCR footprint

Shared log
head/tail



Challenges

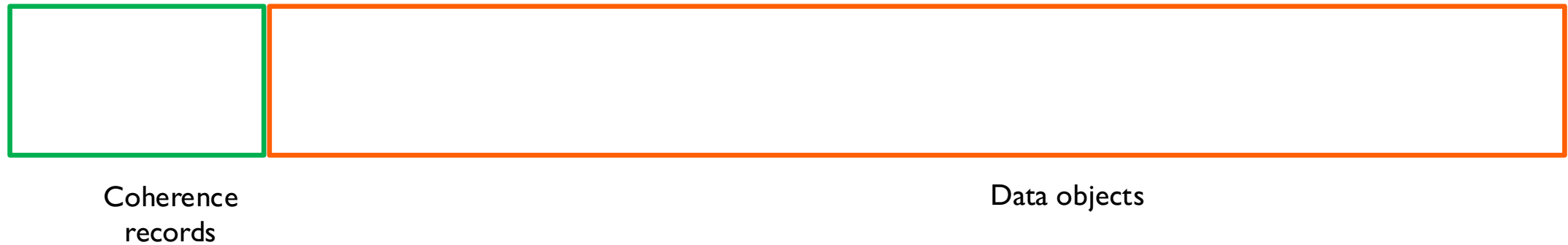
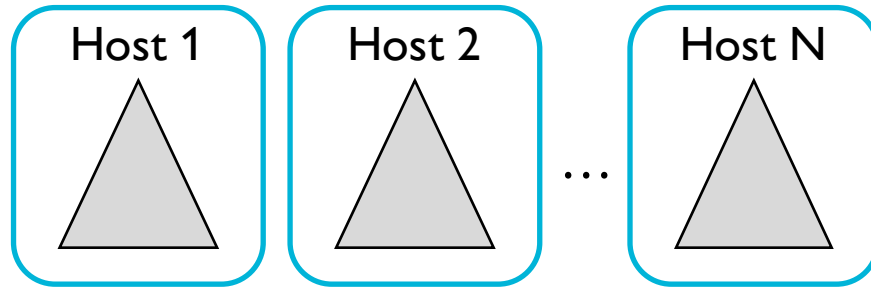
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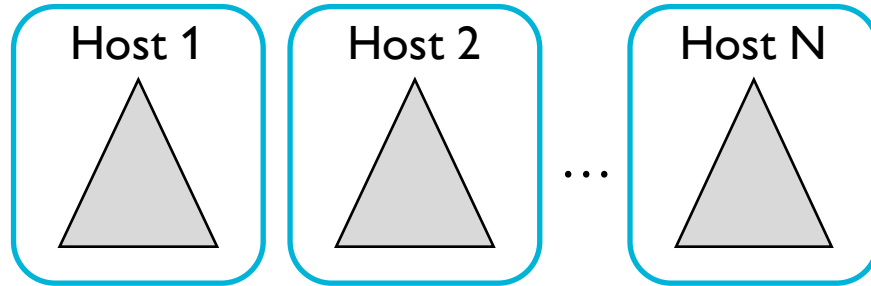
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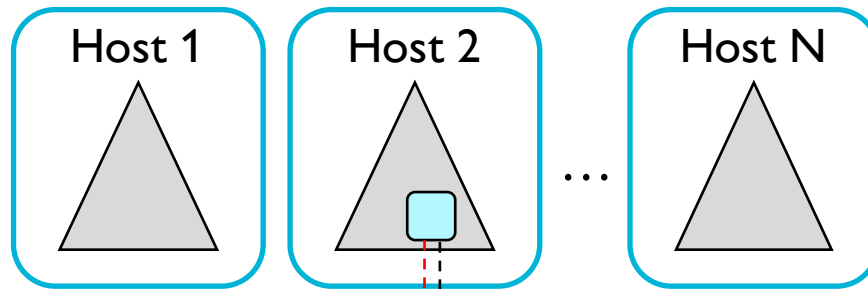
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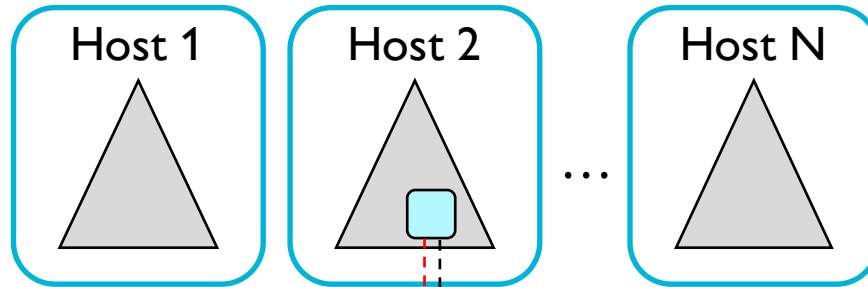


Coherence
records

Data objects

 Read-shared

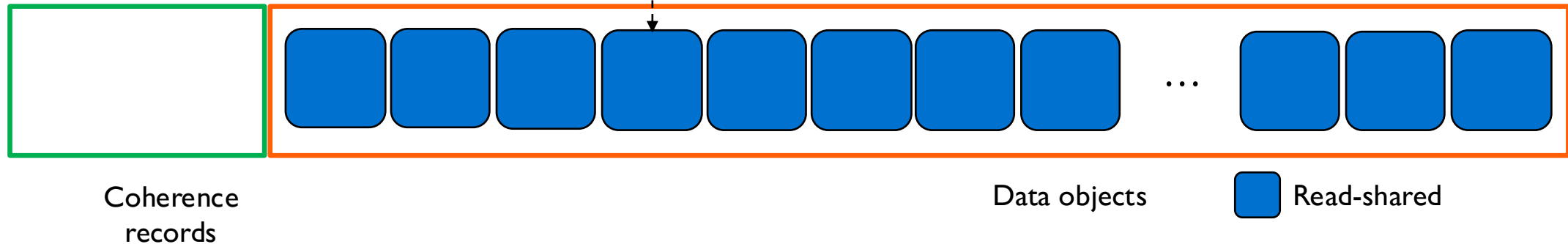
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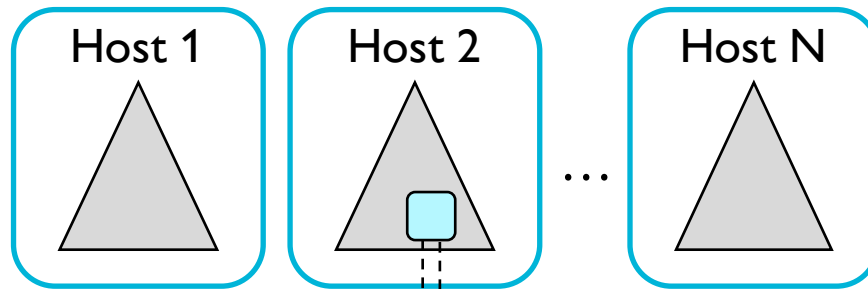
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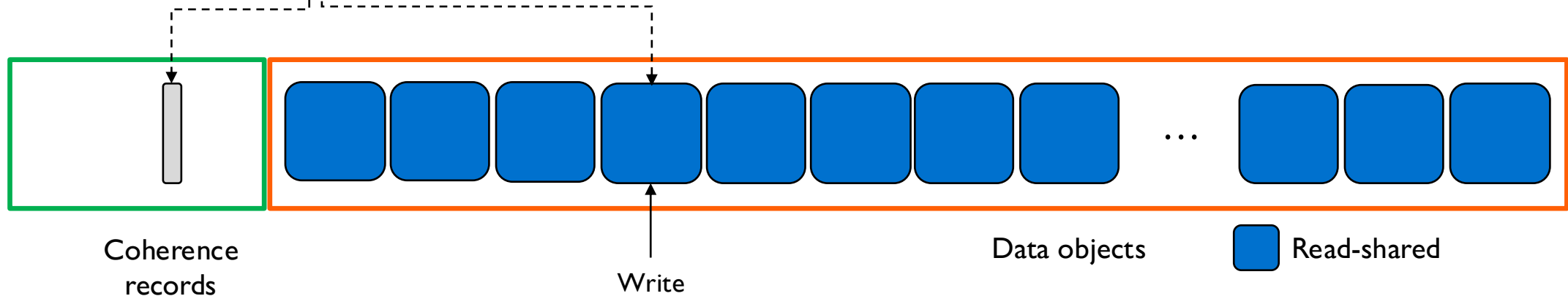


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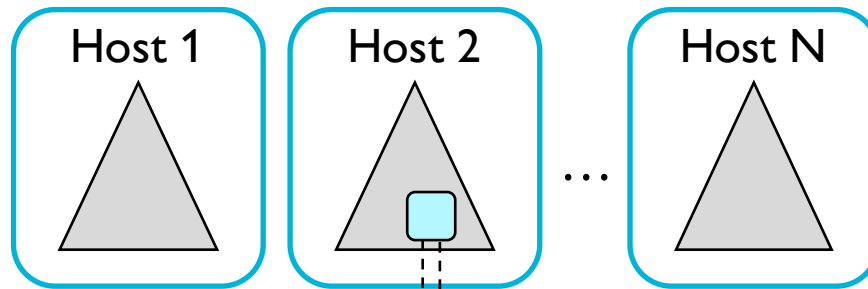
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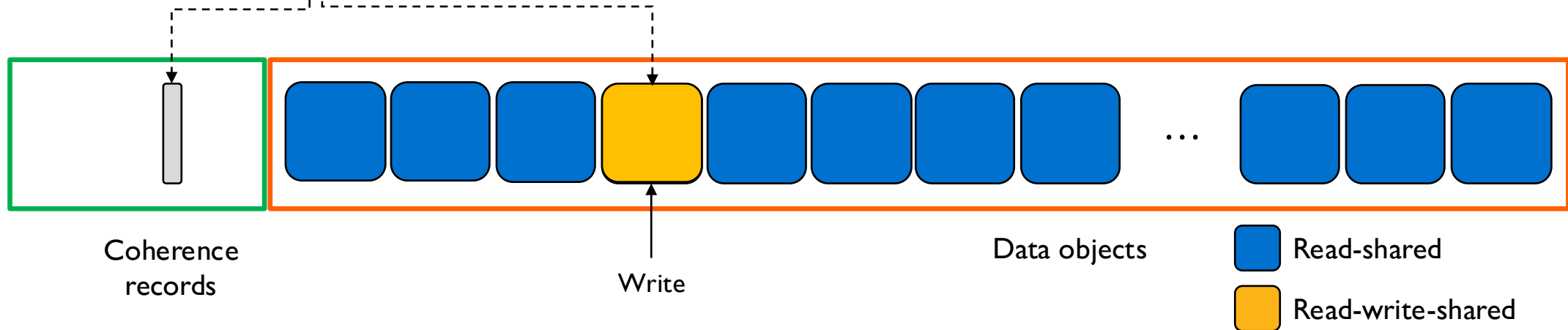


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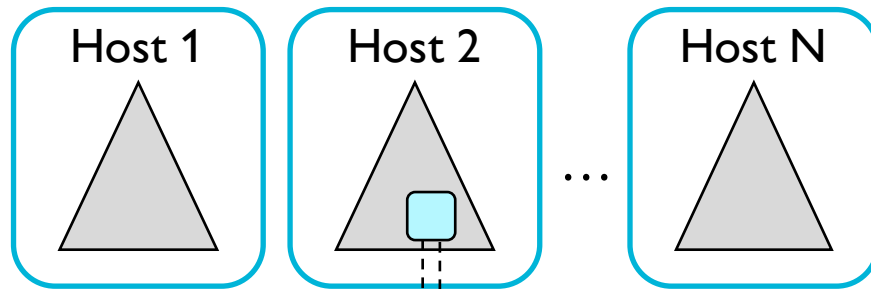
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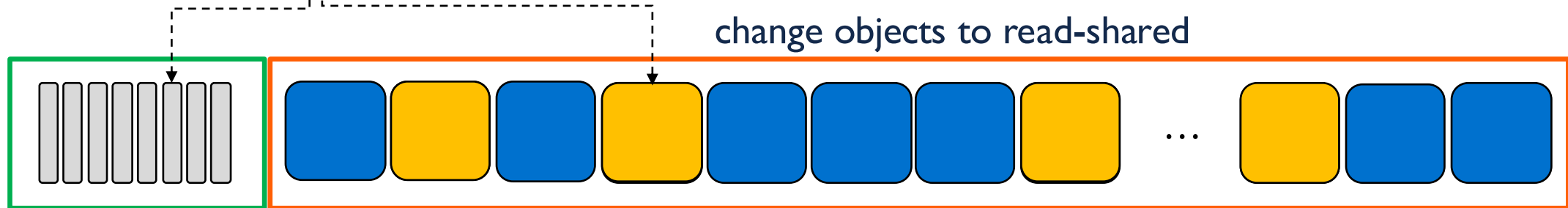
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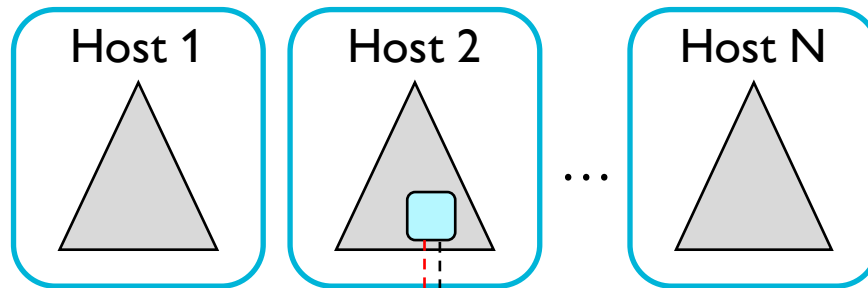
Coherence records

Data objects

Read-shared

Read-write-shared

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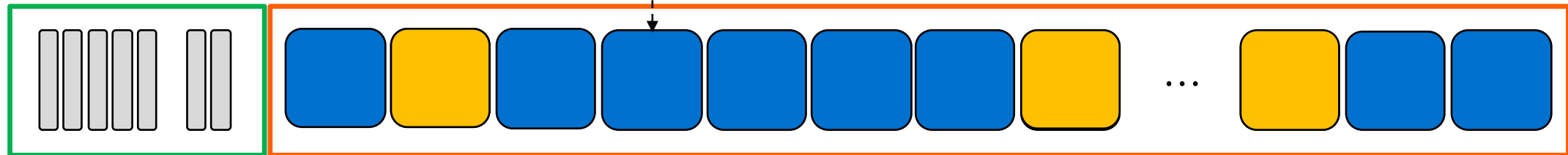
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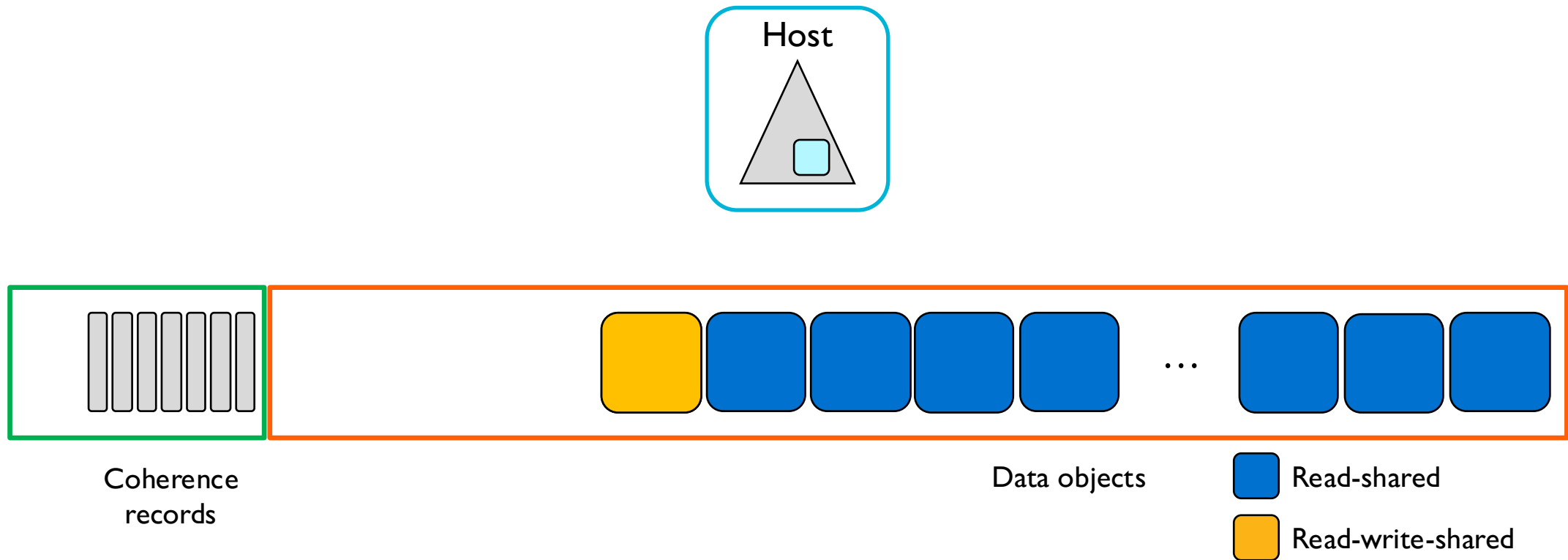
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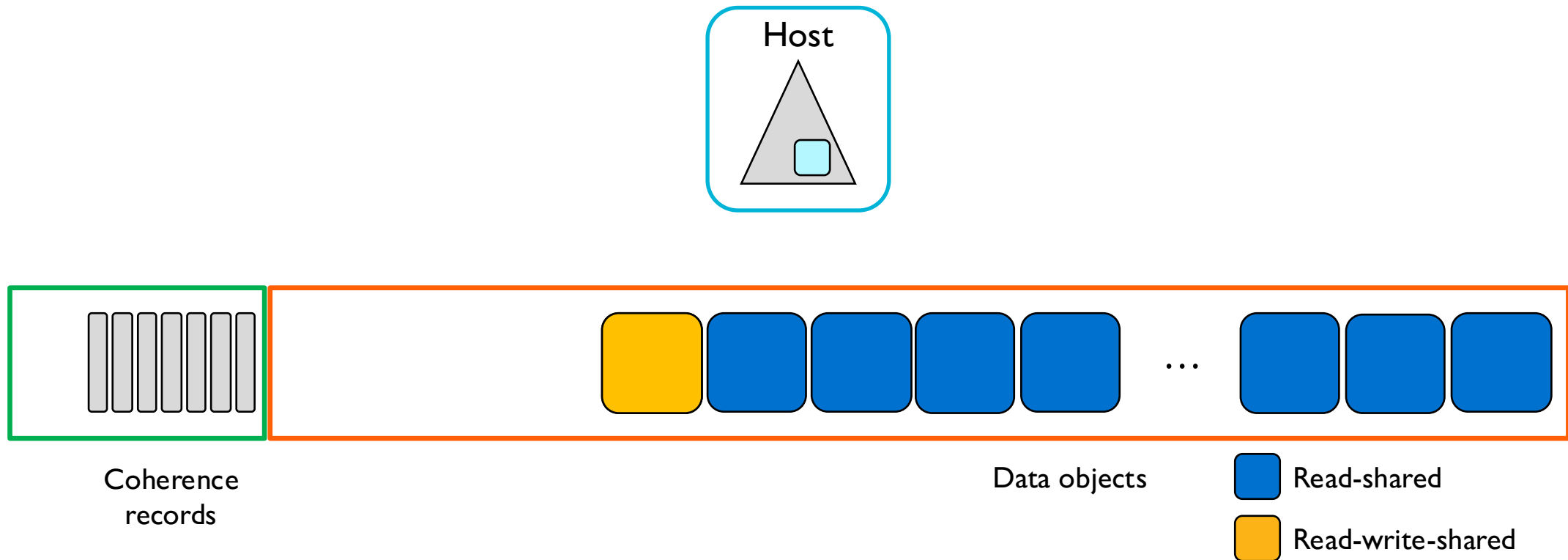
Read-write-shared

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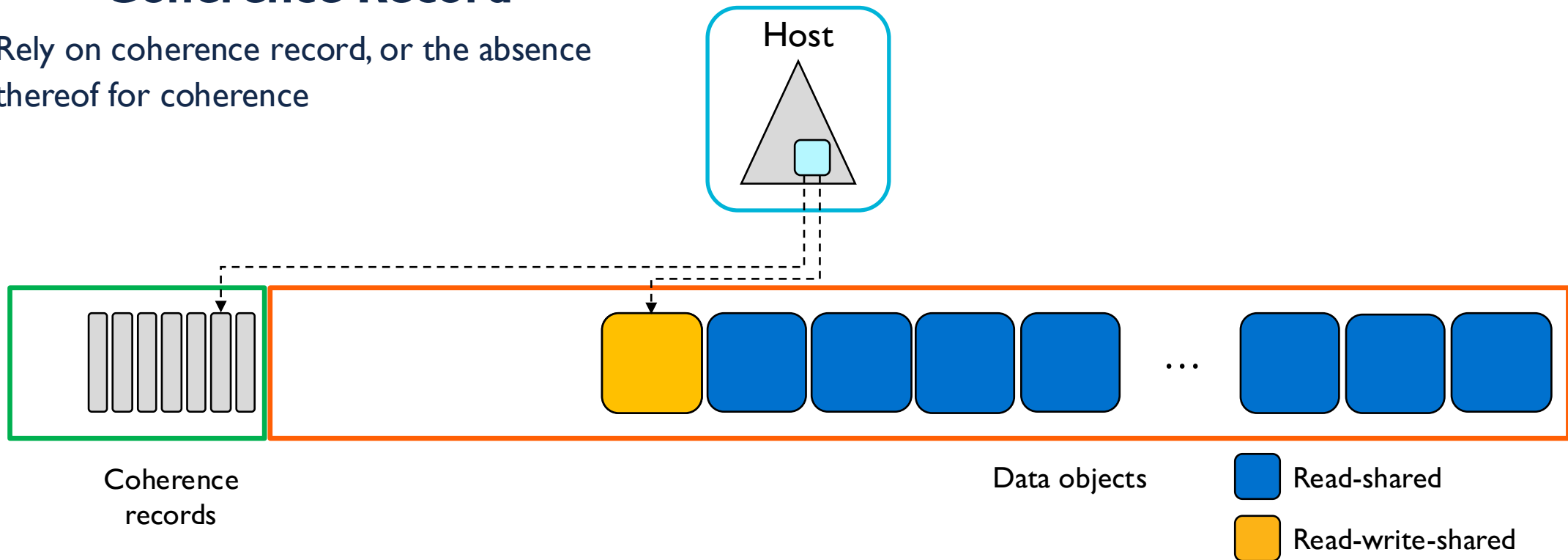


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Rely on coherence record, or the absence thereof for coherence

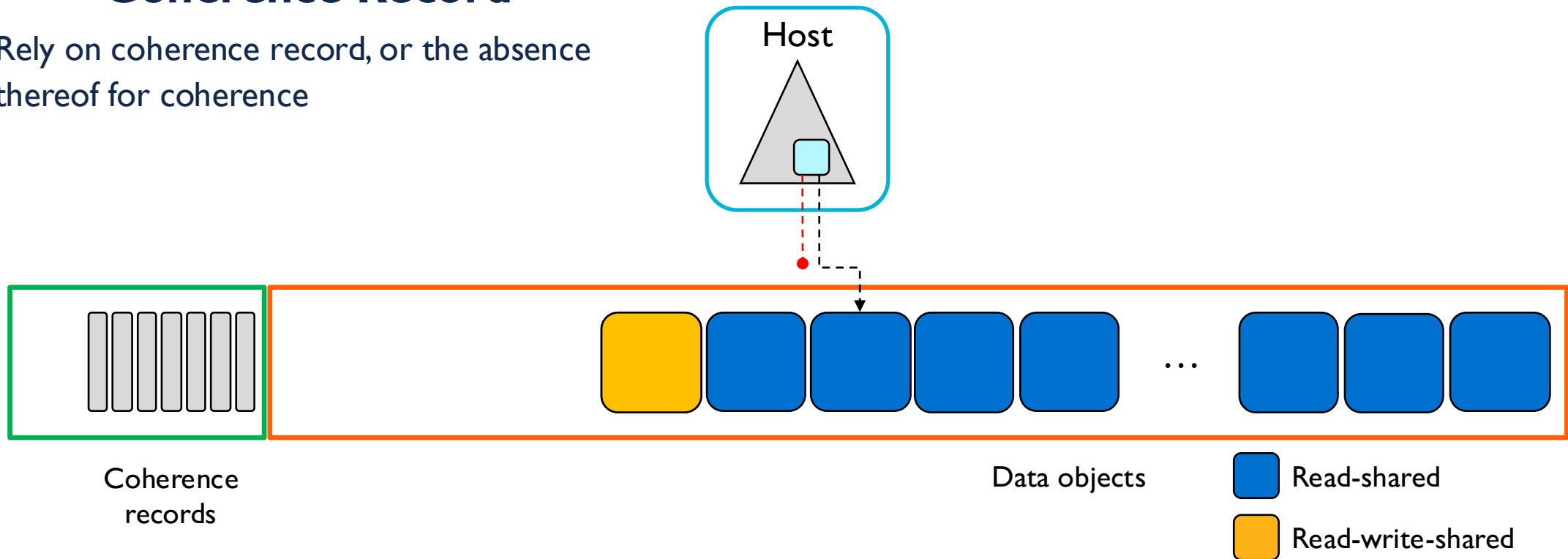


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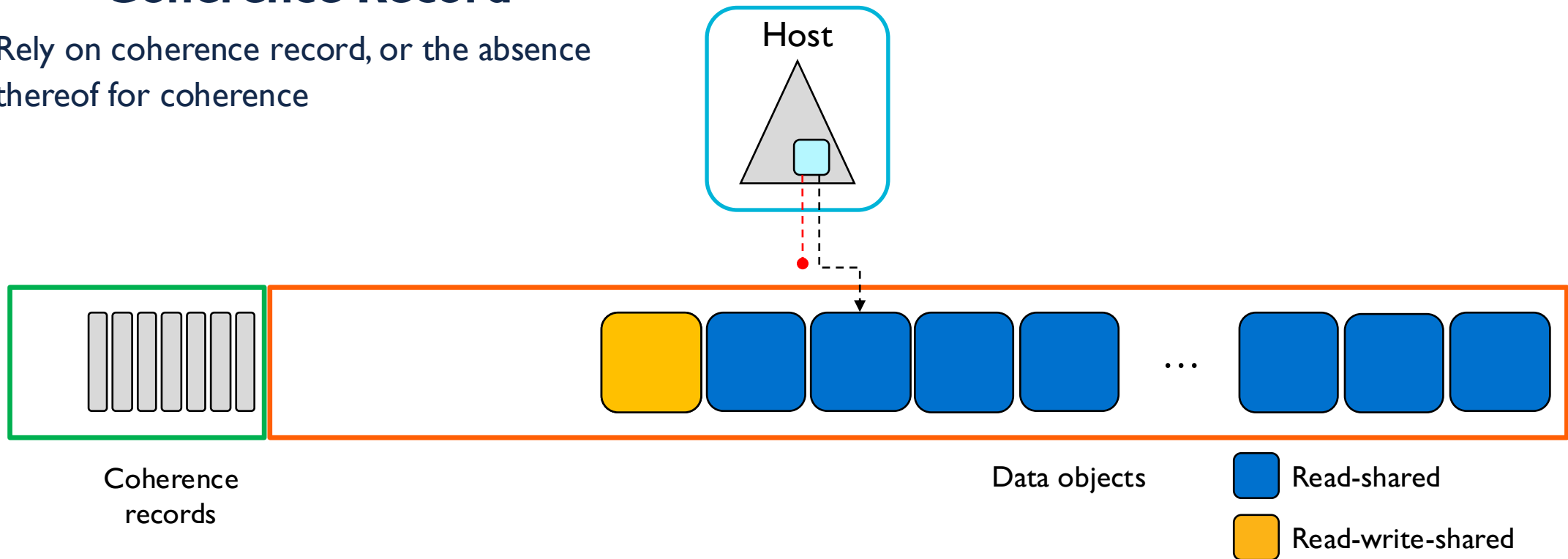


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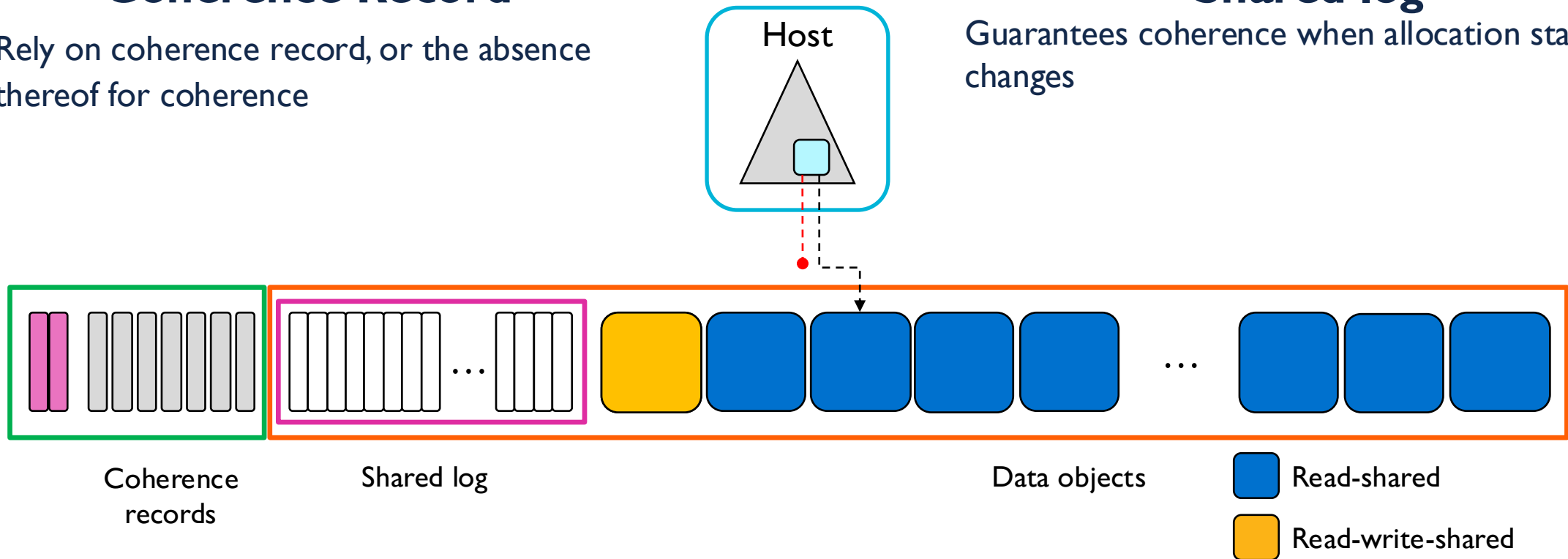
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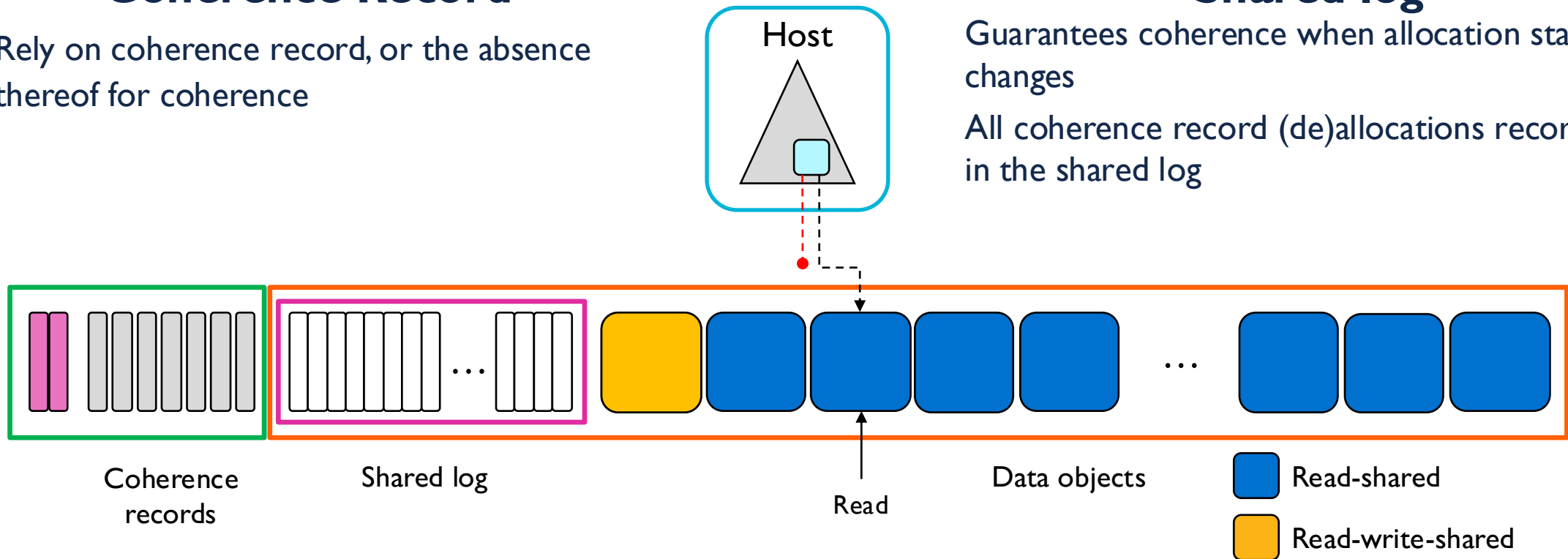
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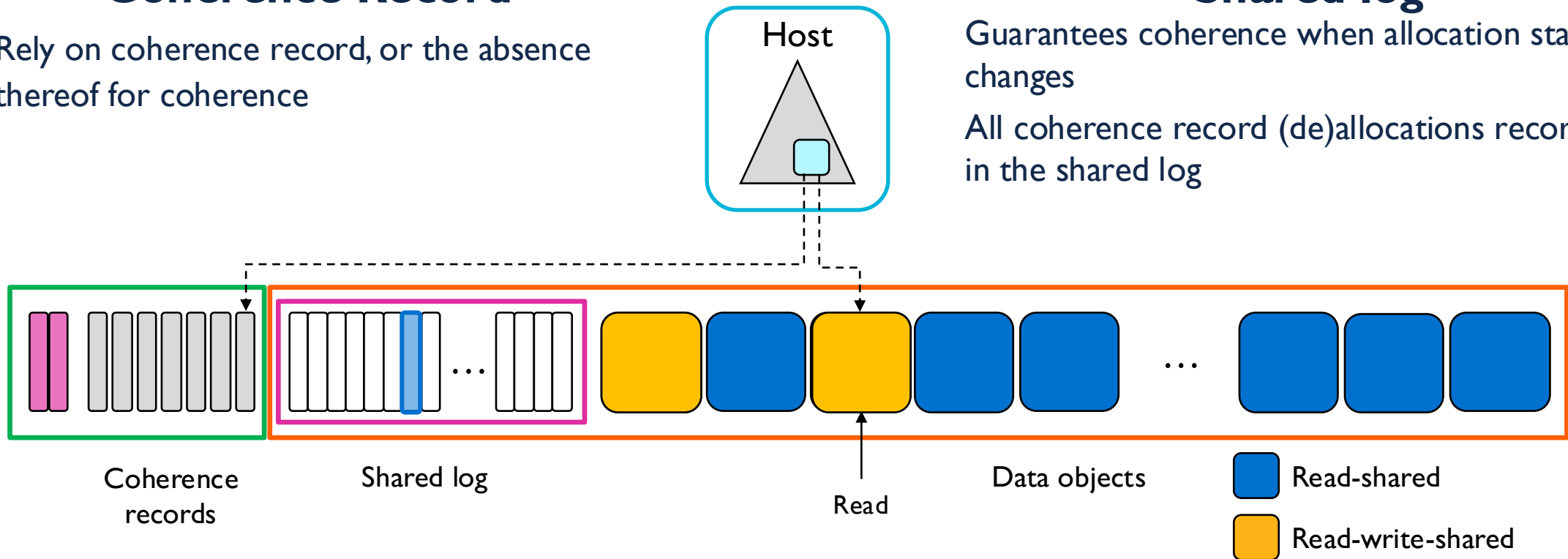
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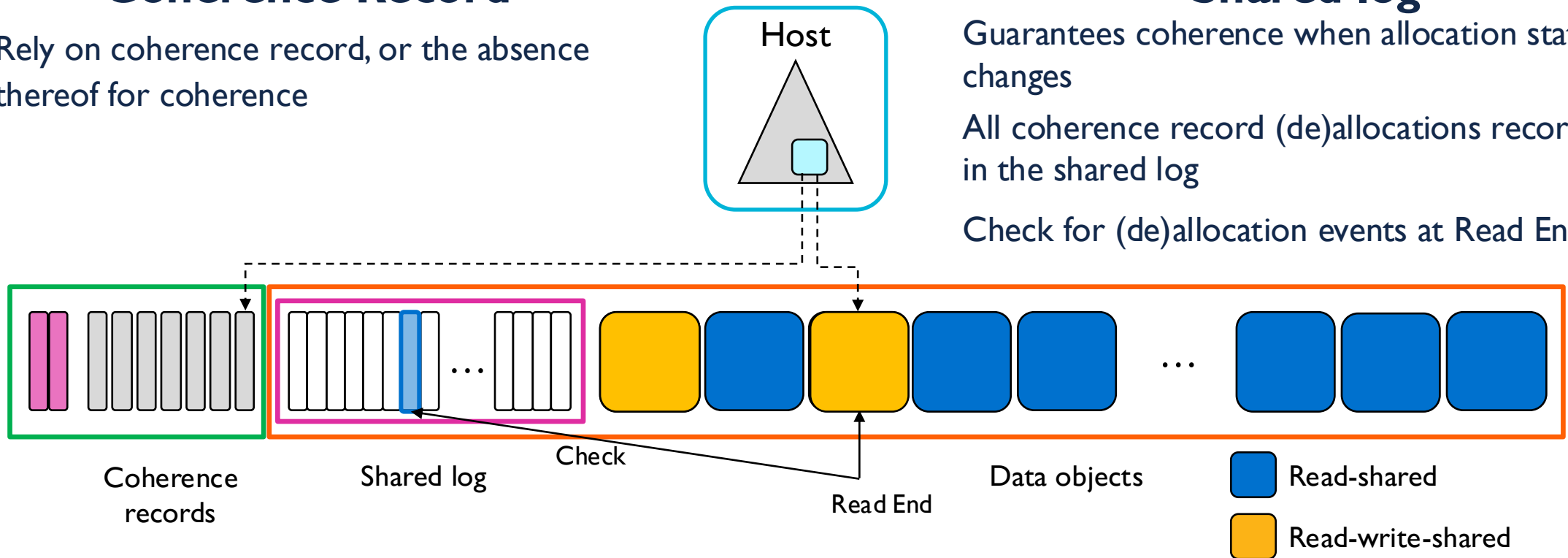
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Check for (de)allocation events at Read End



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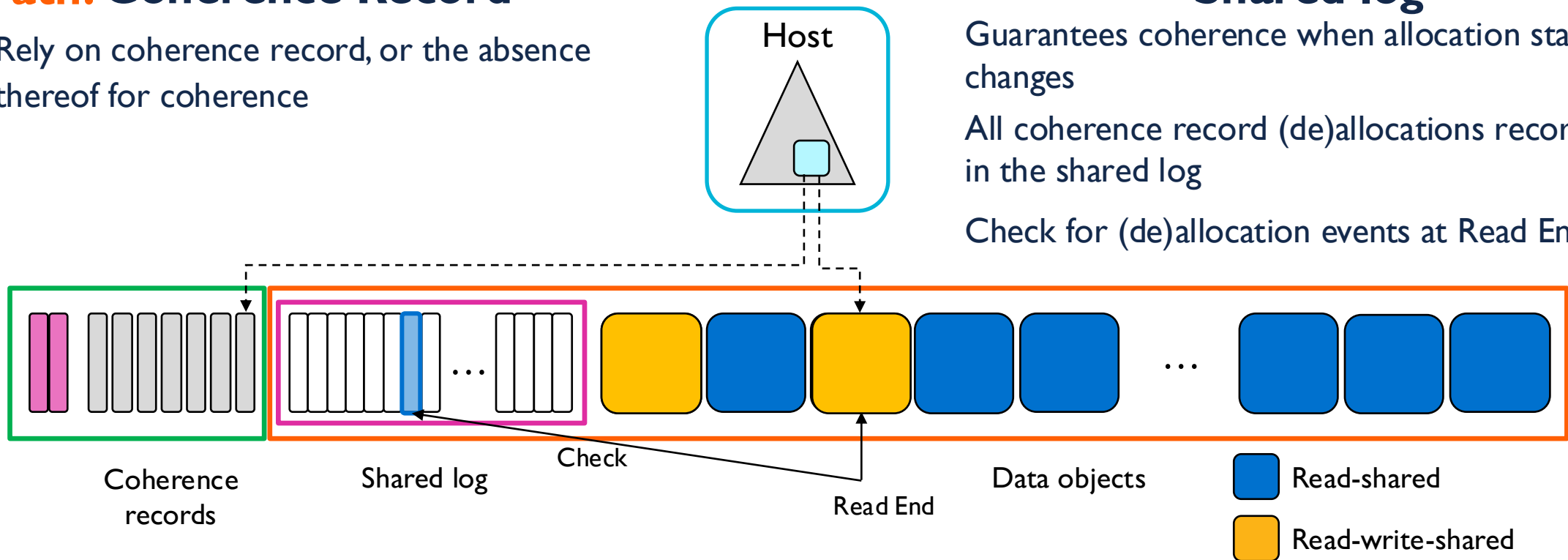
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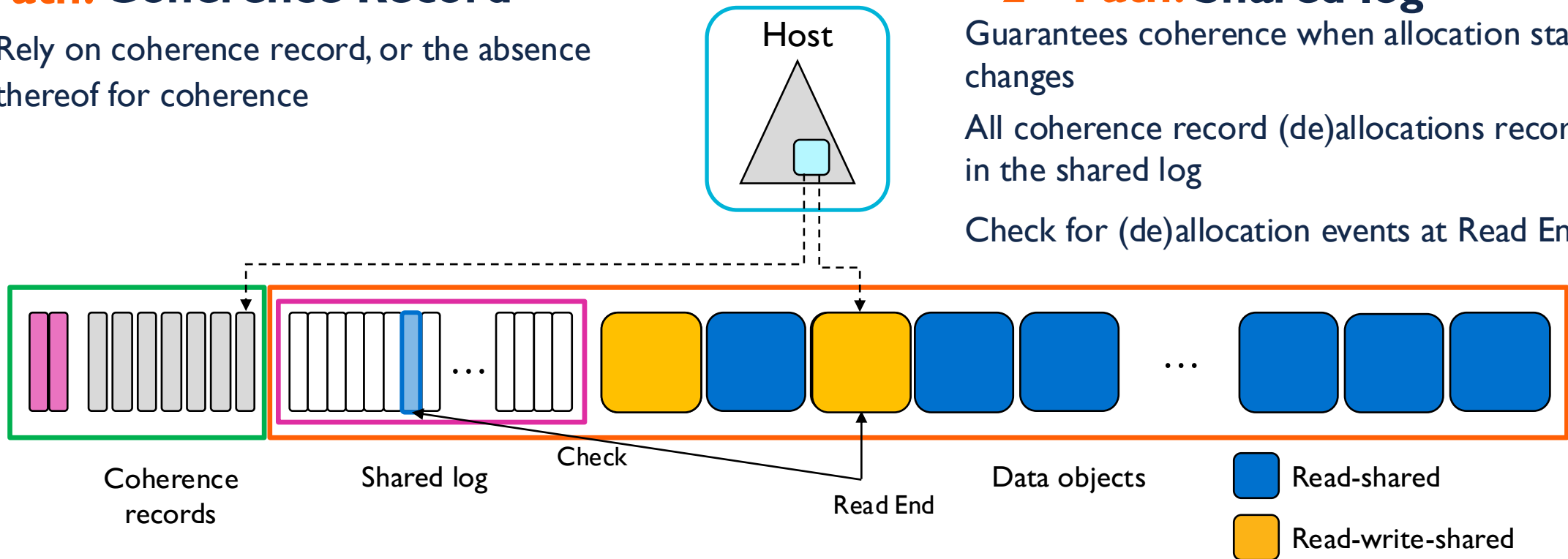
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2nd Path: Shared log

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MEGALON's solution to CXL's high latency

MEGALON supports data replication and tiering to host-local DRAM

Details in the paper



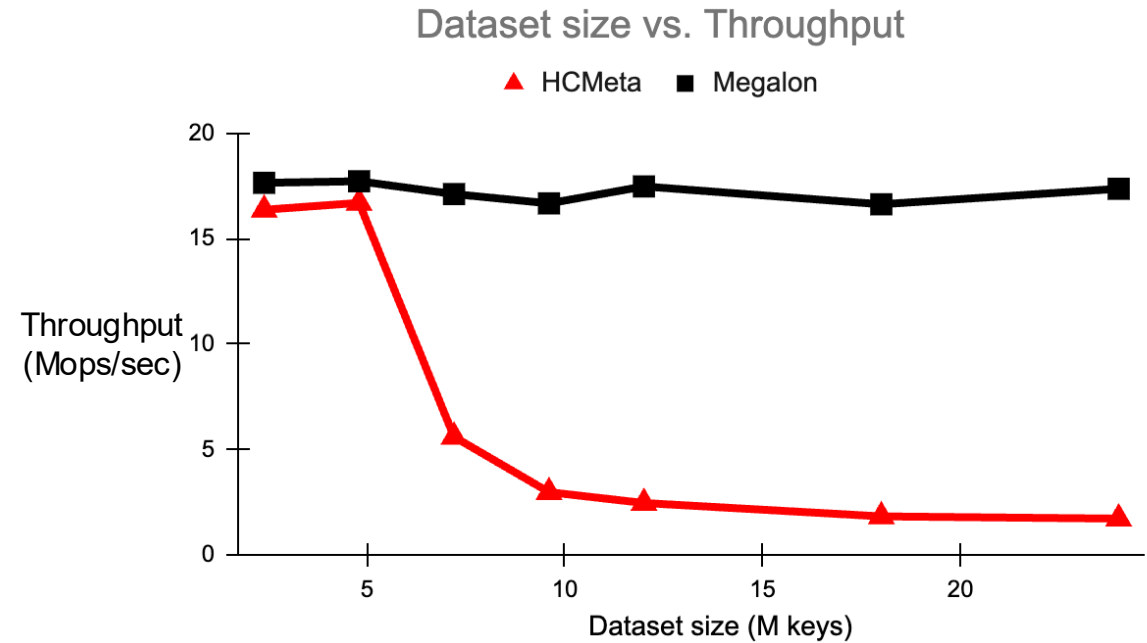
Performance Evaluation

- How does MEGALON perform in read-write workload?
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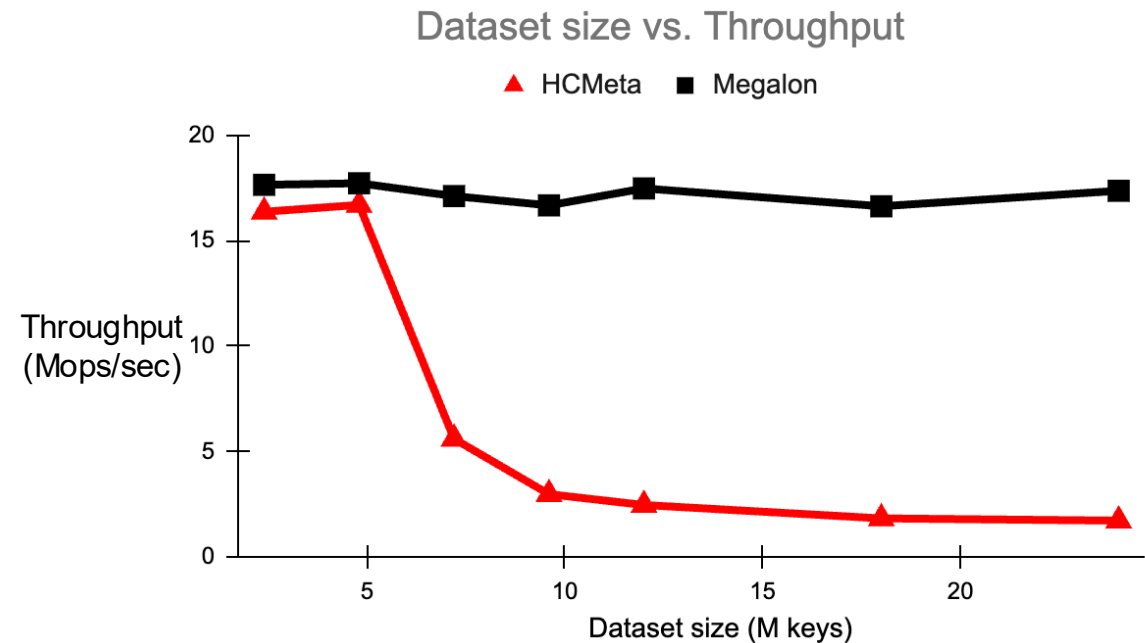
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5% write, Zipfian, 200 MB SCR

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MEGALON allocates coherence records for read-write-shared objects

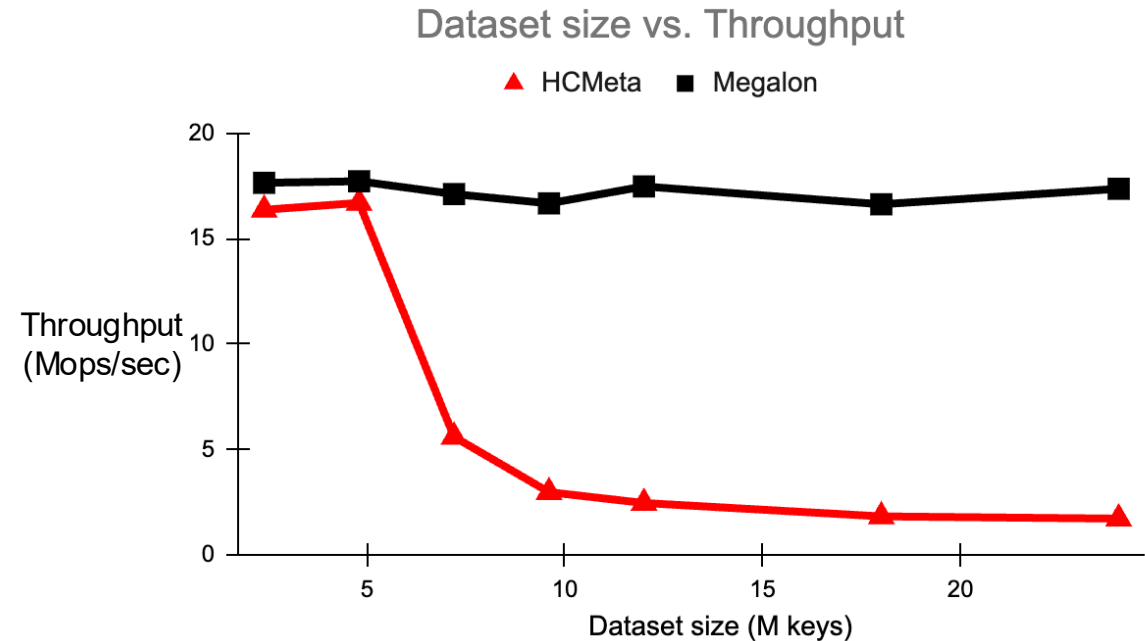


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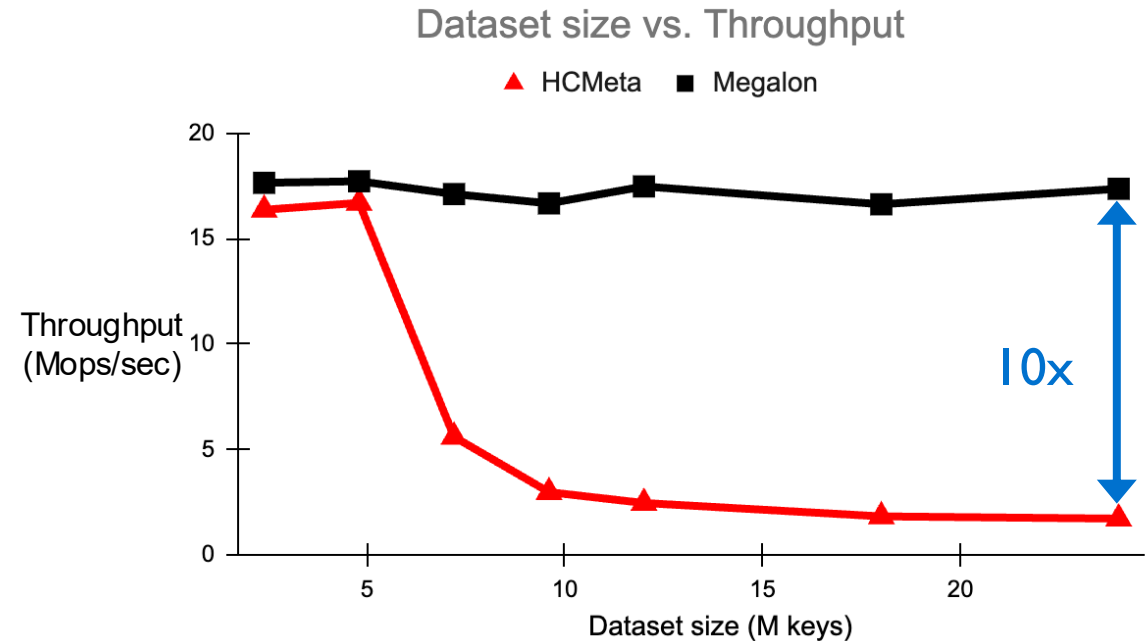
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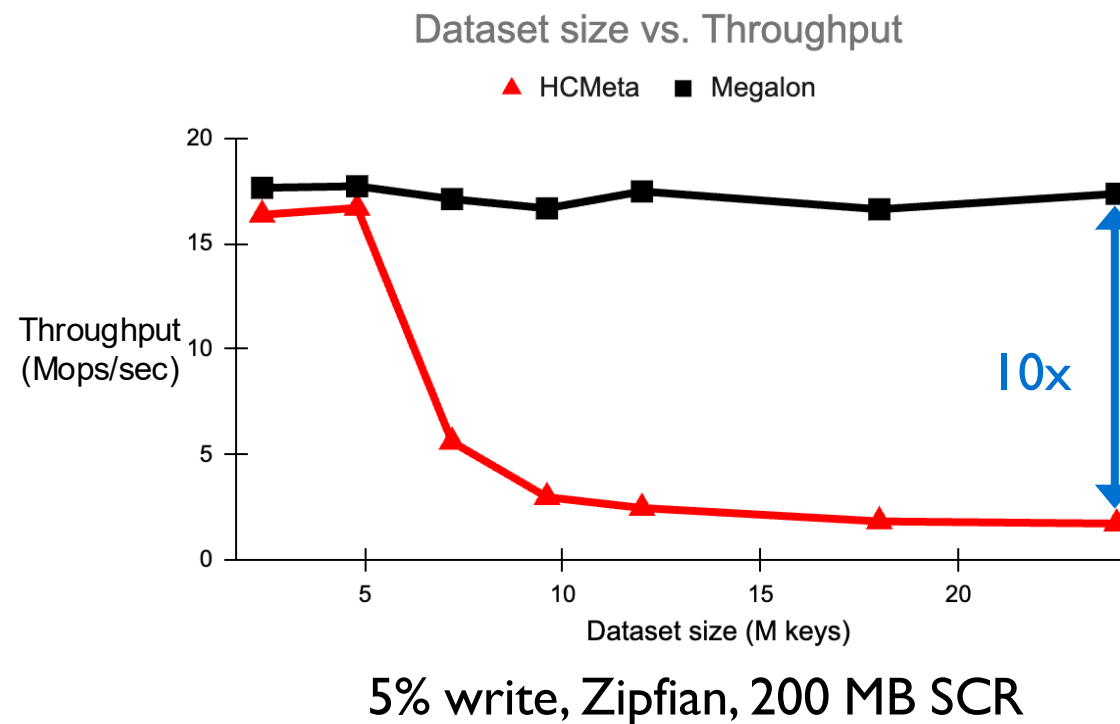
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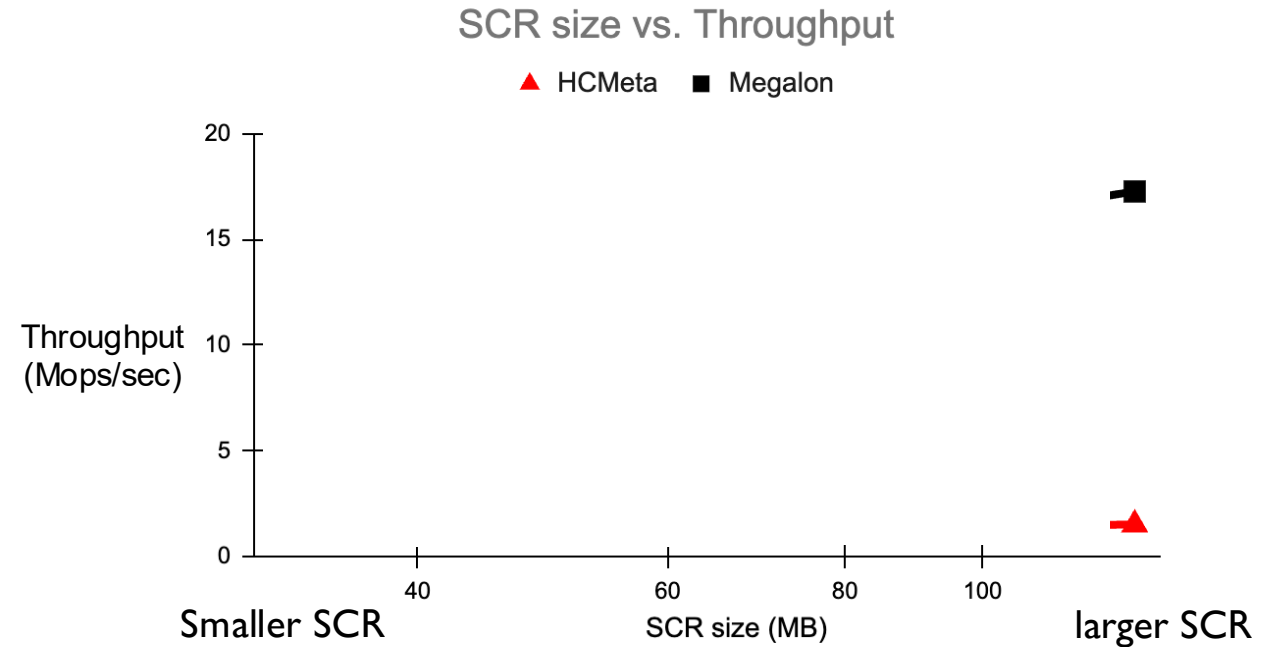
Takeaway: MEGALON significantly increases the number of coherence records that can be shared with **Split Sharing** compared to HCMeta





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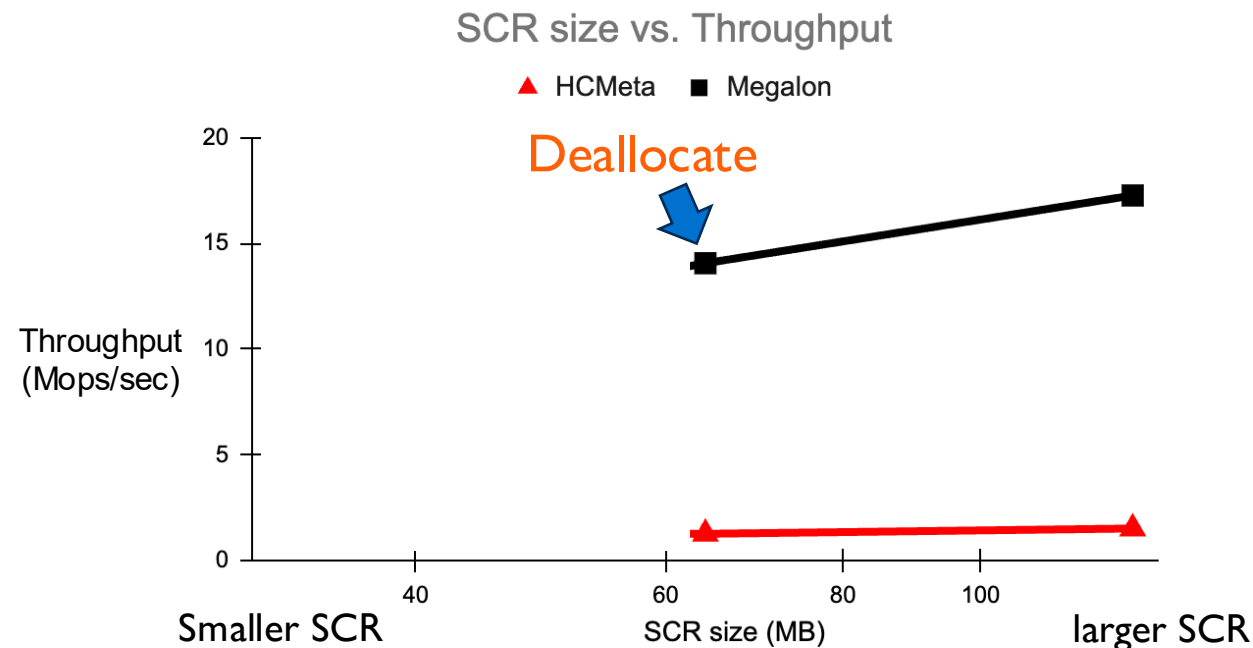
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5% write, Zipfian, 18M objects

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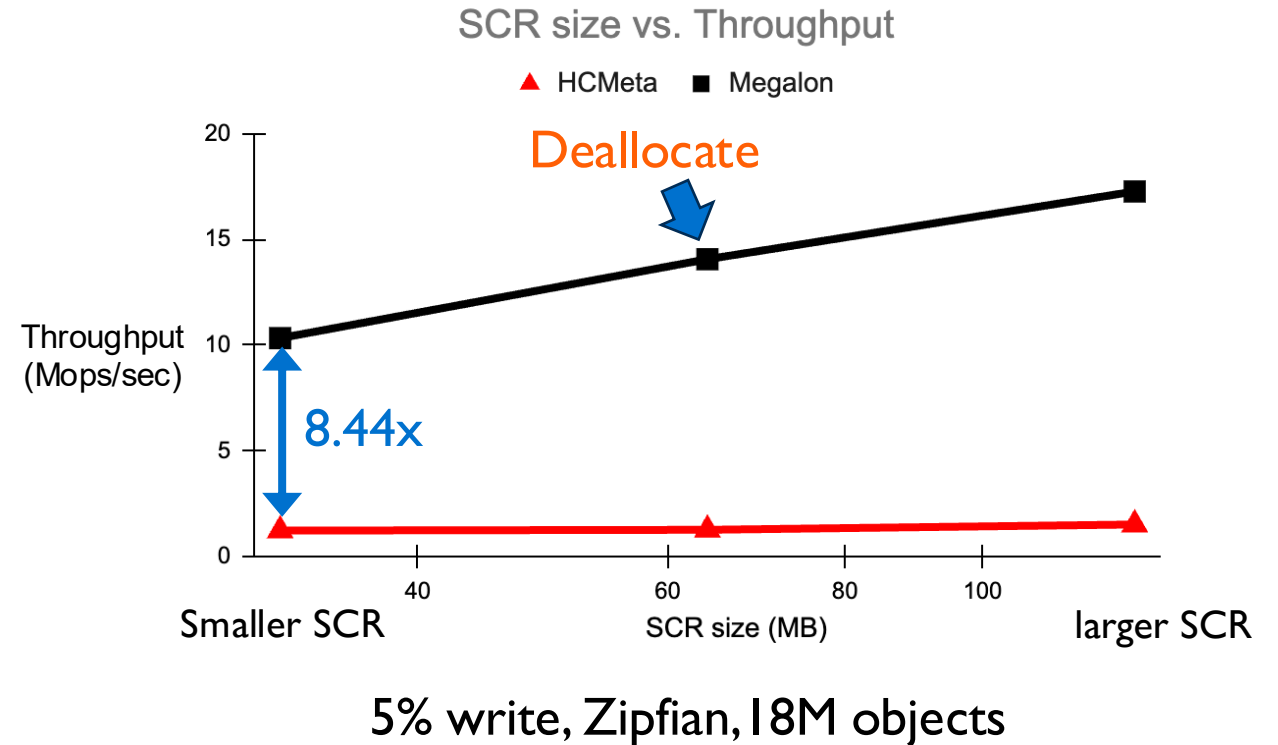
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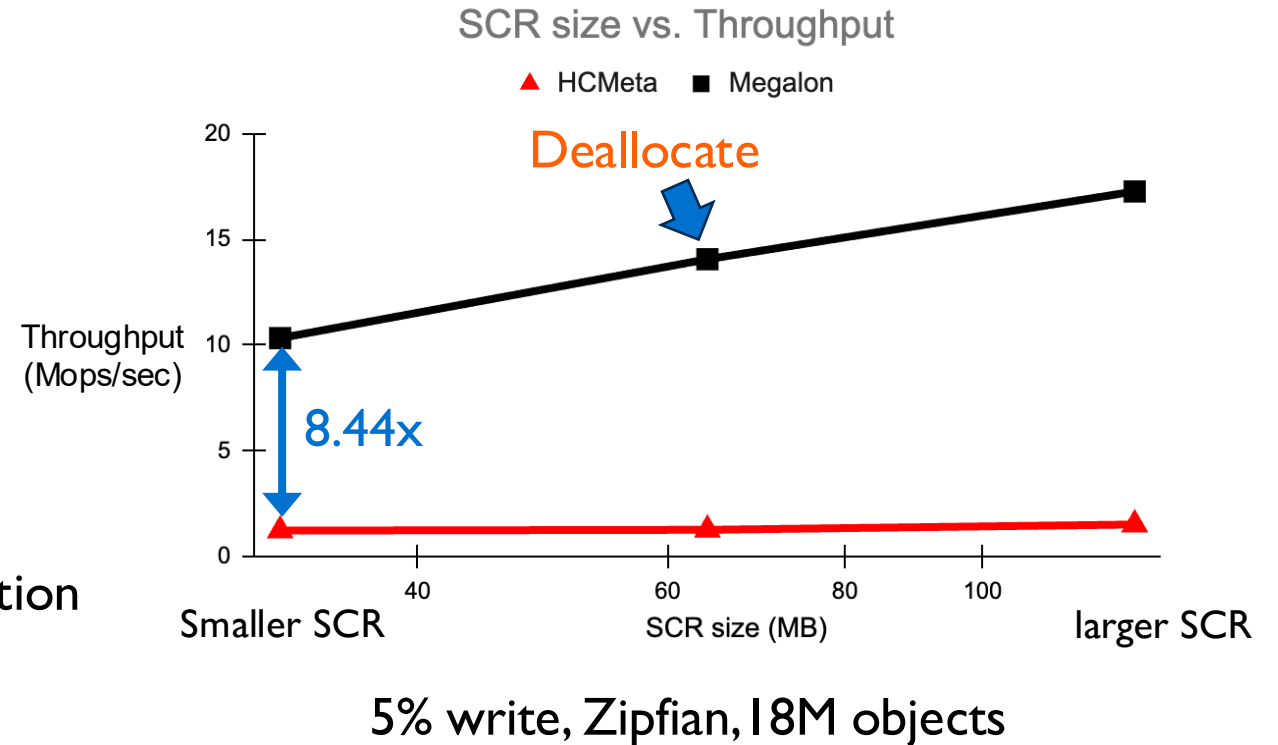
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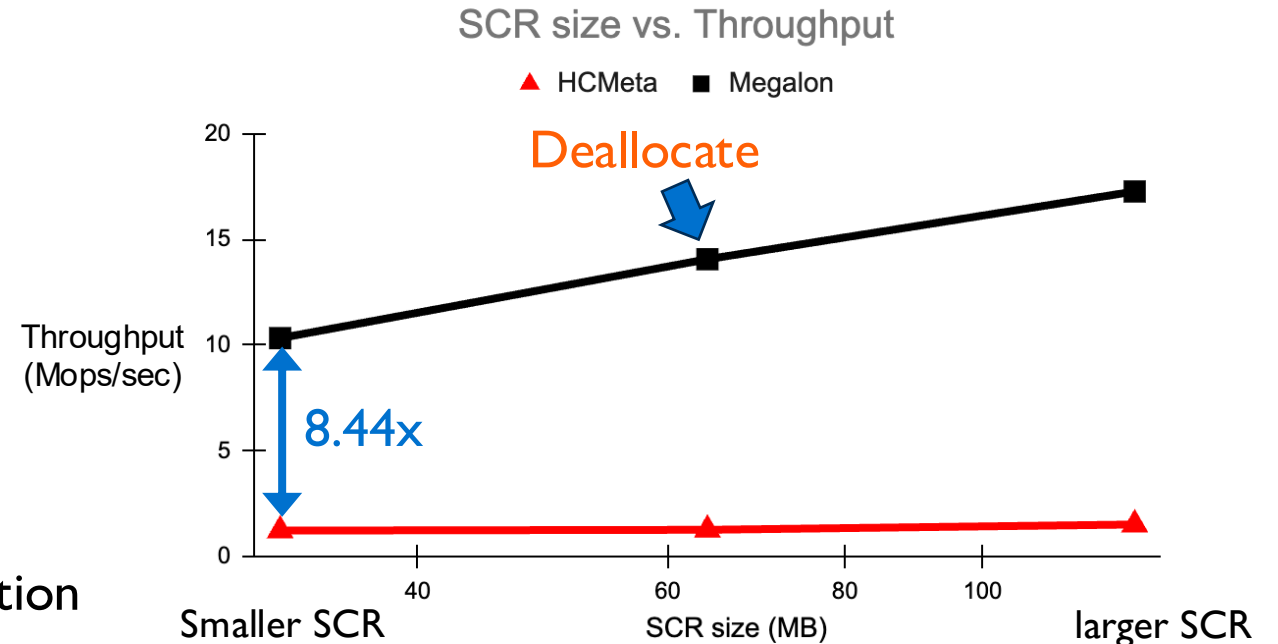
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Takeaway: even when coherence records do not fit in SCR, MEGALON still show performance benefit over HCMeta



5% write, Zipfian, 18M objects



Summary



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Artifact



Backup



The number of objects that can be shared before churn

The number of objects that can be shared before churn

24 Byte key, 200 MB SCR:

- HCMeta: 36 bytes metadata SCR footprint per object; ~5.5 M objects
- Megalon: 4 bytes (32-bit coherence record) SCR footprint per object; ~50M read-write-shared objects & unlimited-read shared objects



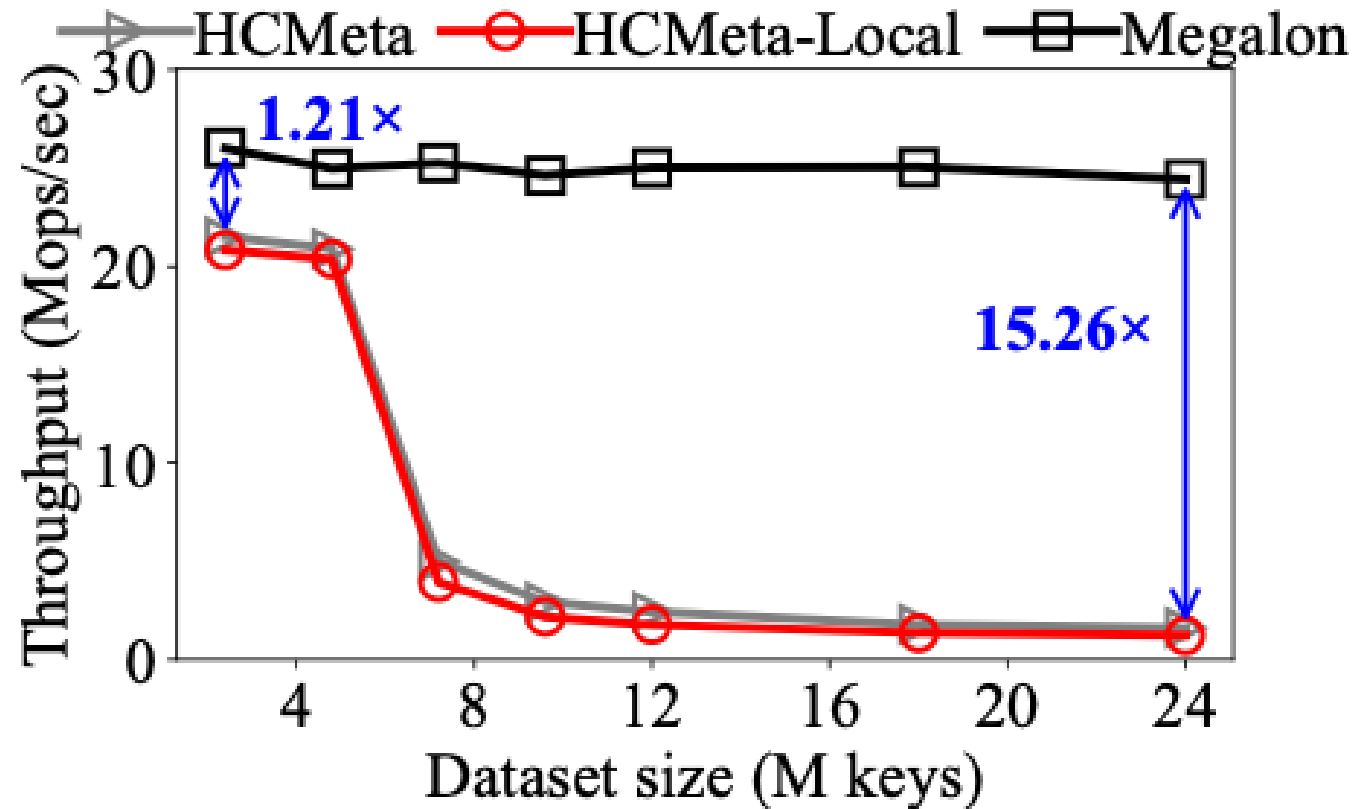
Memory overhead

Memory overhead

24 Byte key, 1KB data object, 24M object, 200 MB SCR:

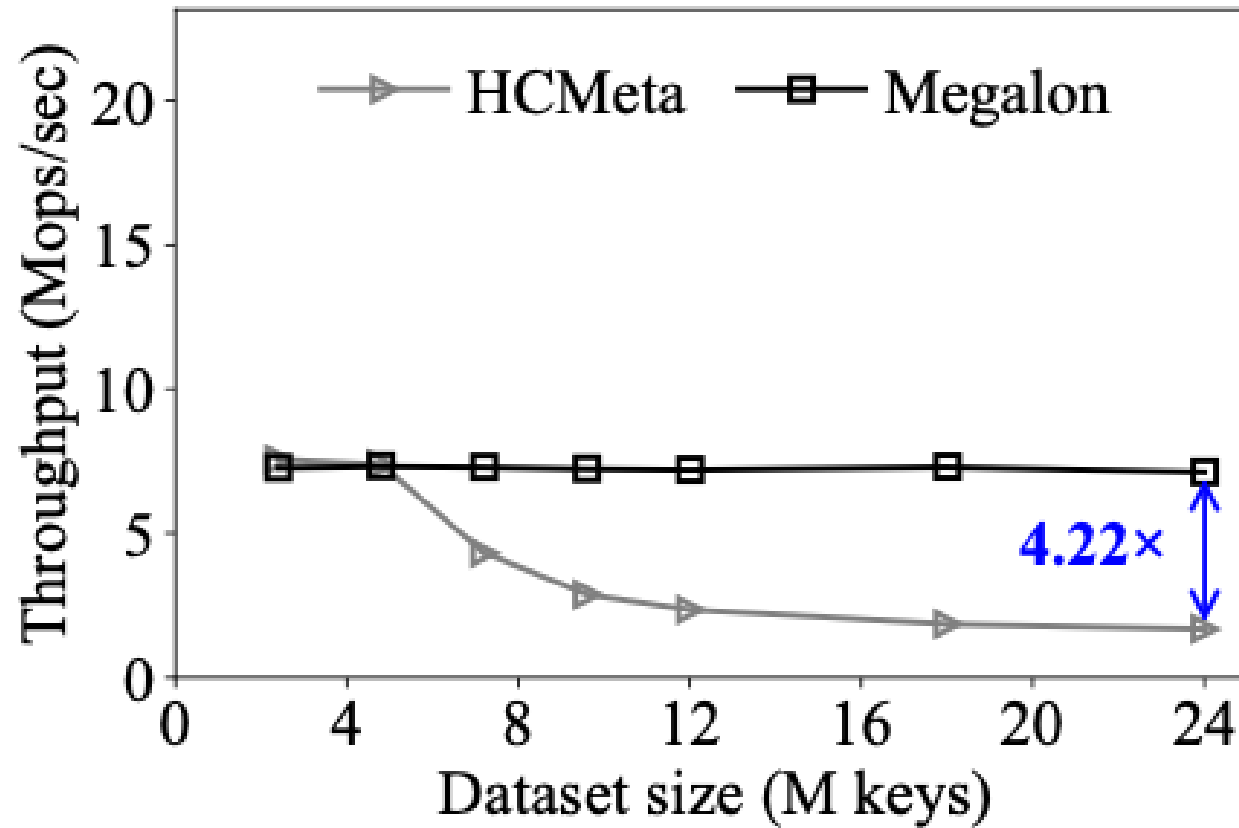
- HCMeta: 25.76 GB total memory footprint, 1.6 MOps/s
- Megalon: 27.71 GB total memory footprint, 24.4 Mops/

How does MEGALON perform in read-only workload?



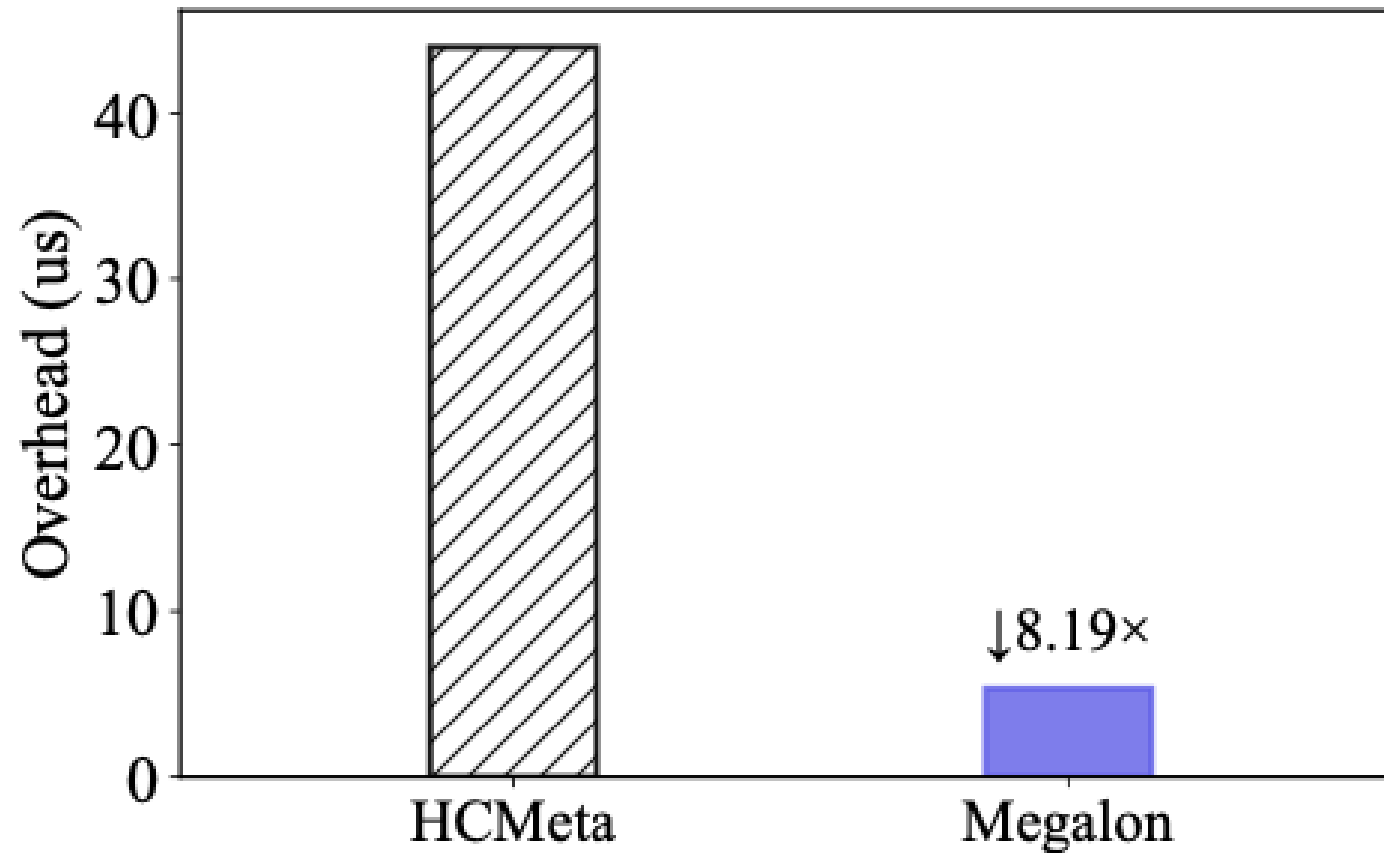
Read-only, Zipfian, 200 MB SCR

How does MEGALON perform in 50%-write workload?



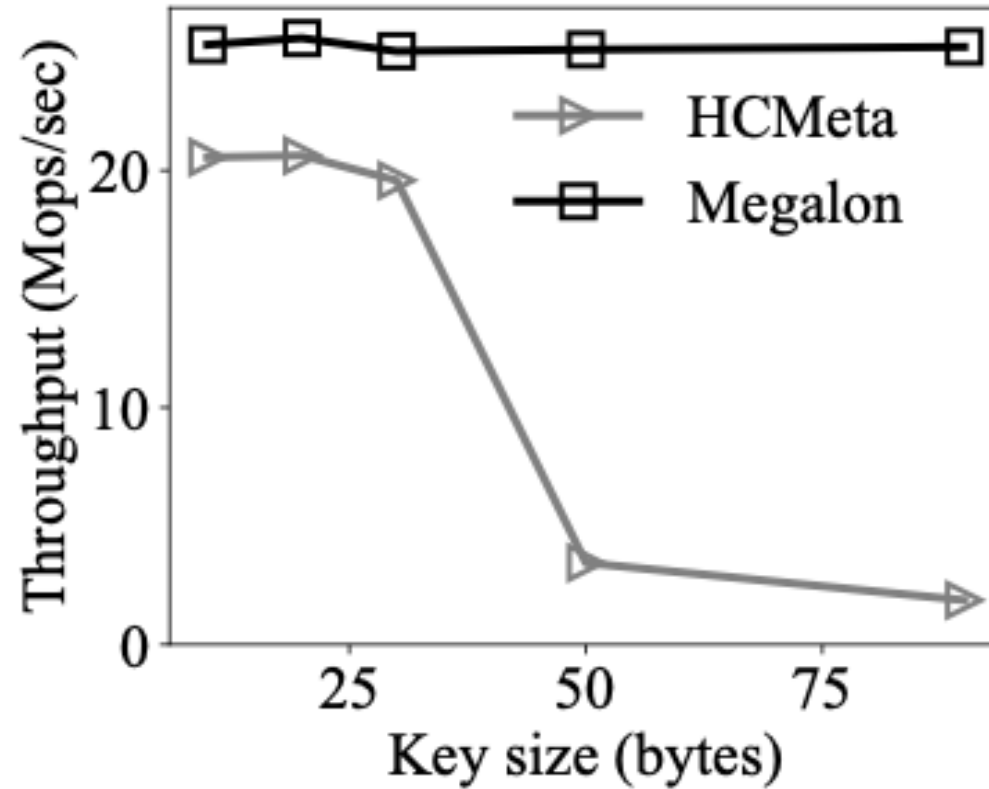
50% write, Zipfian, 200 MB SCR

Overhead of Churns



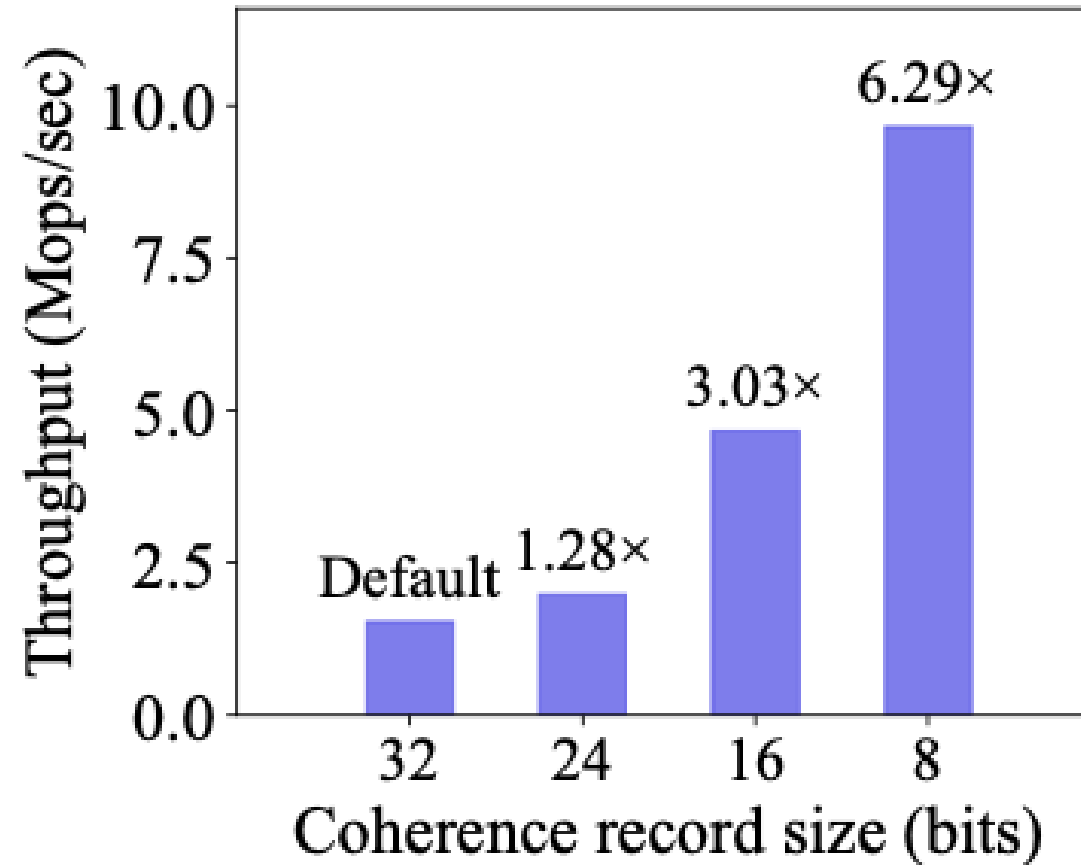
Data object (un)sharing vs. coherence records (de)allocation

Effect of Different Key Sizes



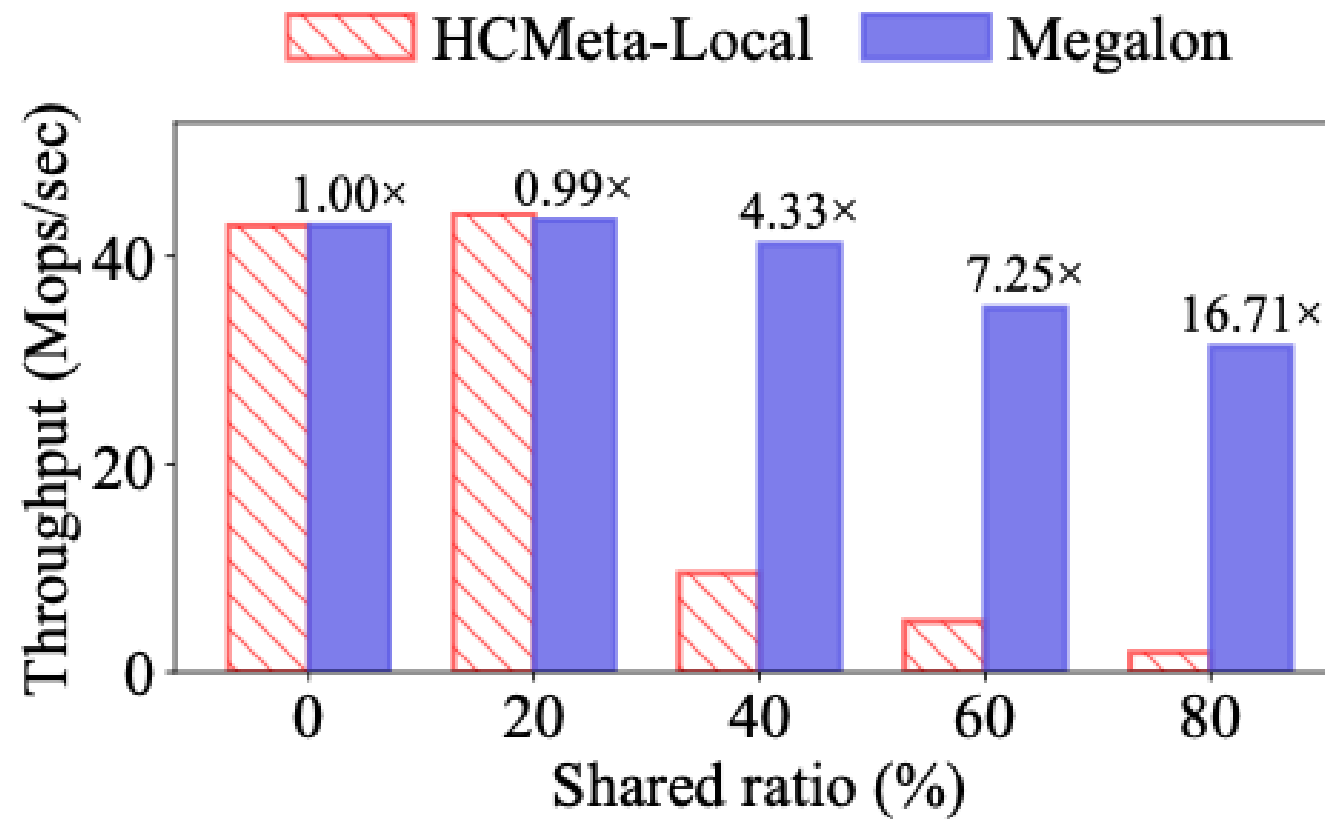
Read only , Zipfian, 200MB SCR, 4.8M objects

Effect of Different Coherence Record Sizes



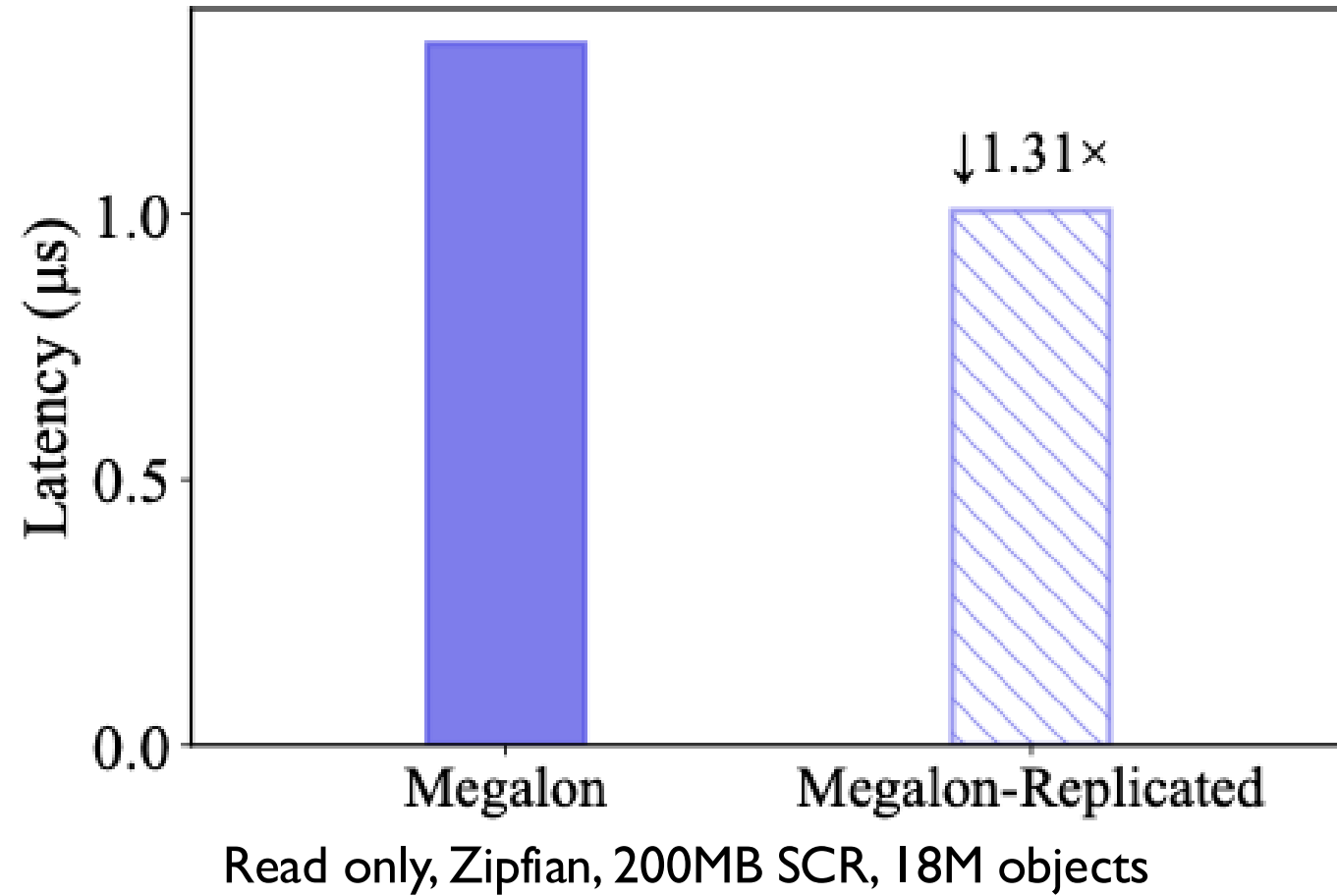
50% write, Zipfian (less skew), 32MB SCR, 18M objects

Local Partition

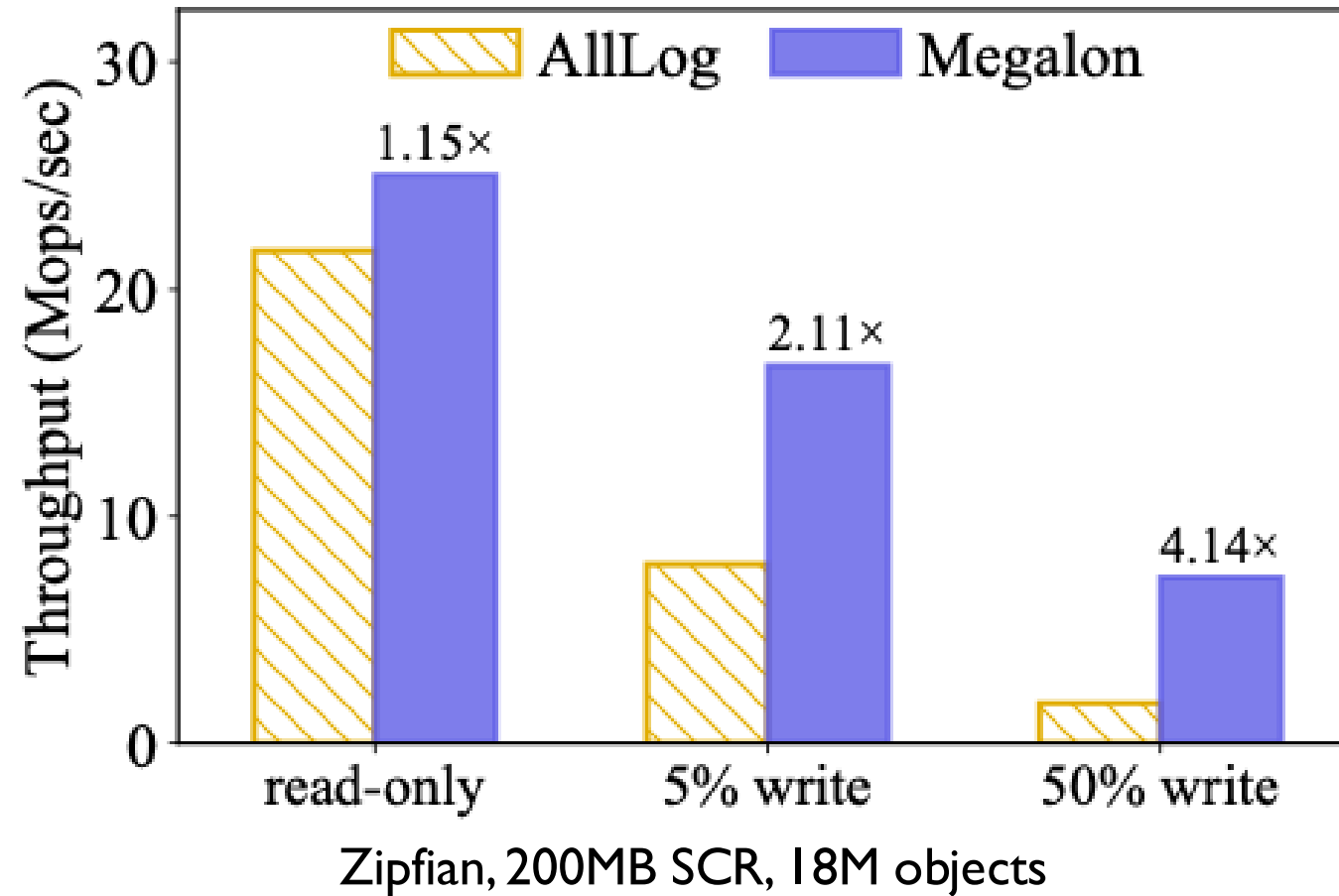


Read only, Zipfian, 200MB SCR, 18M objects

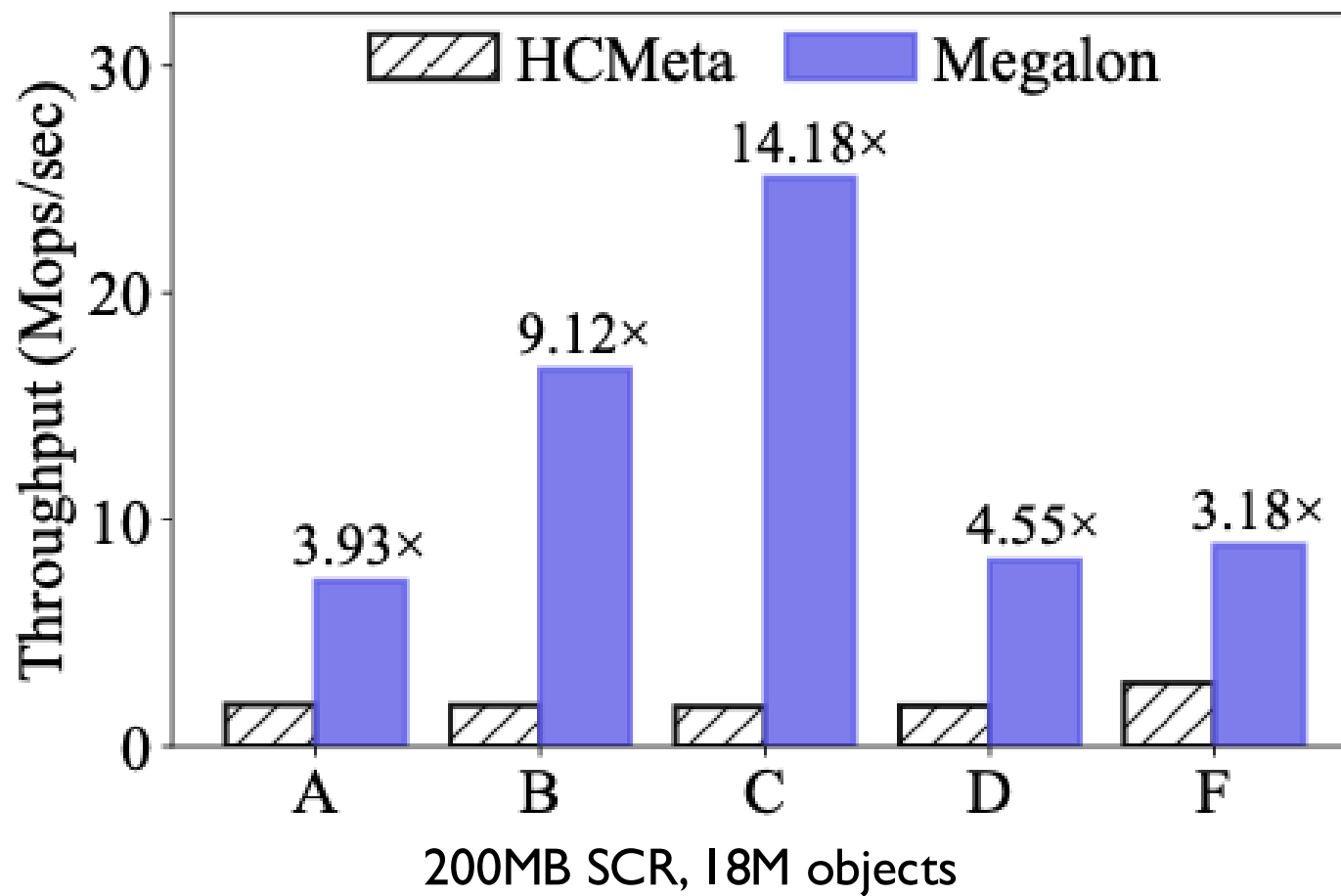
Local Replication



What if Coherence Record is also Logically Replicated?



YCSB Workloads



How does MEGALON perform in page-cache application?

Workload: 48 GB dataset size, 4KB page size, Zipfian

MEGALON increases system throughput

- By 5.7x compared to HCMeta in read-only workload
- By up to 4.5x in read-write workload

